

Compal confidential

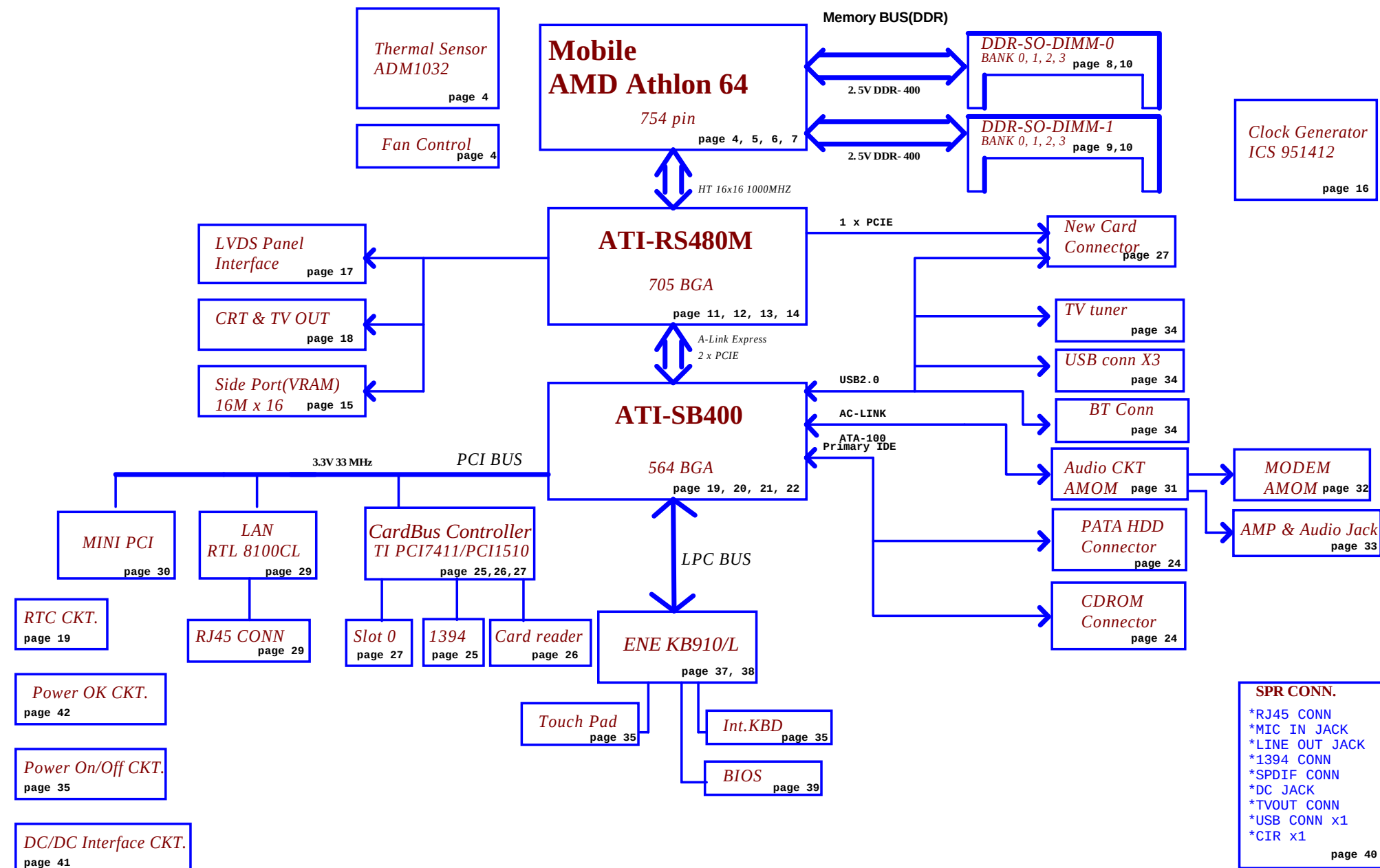
Schematics Document

AMD K8 with
ATI RS480M+ATI SB400

2005-08-29
REV:0.8



Security Classification		Compal Secret Data		Title	
Issued Date	2005/03/01	Deciphered Date	2005/04/06	Cover Page	
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Voltage Rails

power plane State	+12VALW +5VALW +3VALW +1.8VALW	+5V +2.5V +1.25V	+5VS +3VS +2.5VS +1.8VS +1.5VS +2.5VDDA +CPU_CORE +1.2V_HT
S0	0	0	0
S1	0	0	0
S3	0	0	X
S5 S4/AC	0	X	X
S5 S4/AC don't exist	X	X	X

0 MEANS ON

X MEANS OFF

PCI Devices

INTERNAL DEVICE	IDSEL #	REQ/GNT #	PIRQ
SMBUS			
IDE			A
LPC I/F			
PCI to PCI			
AC97 AUDIO			B
AC97 MODEM			B
OHCI#1 USB			D
OHCI#1 USB			D
EHCI USB			D
SATA#1			A
SATA#2			A

EXTERNAL			
Wireless LAN	AD18	3	F
LAN	AD22	1	G
CARD BUS & 1394	AD20	2	E,H

BOM STATUS :

VRAM@ ,VRAMIC@, SAMSUNG@, HYNIX@, 2HDD@ ,7411@ ,EXP@ ,17_EXP@ ,15_EXO@,CIR@ ,D@, C@, 15.4@, DOCK@, WL_LED@

45@ (for 45 level RTC battery)

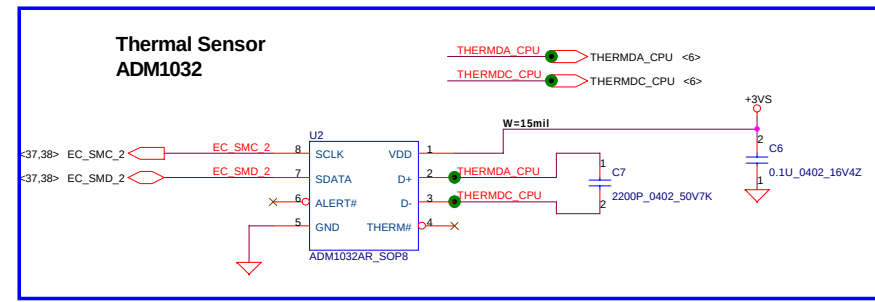
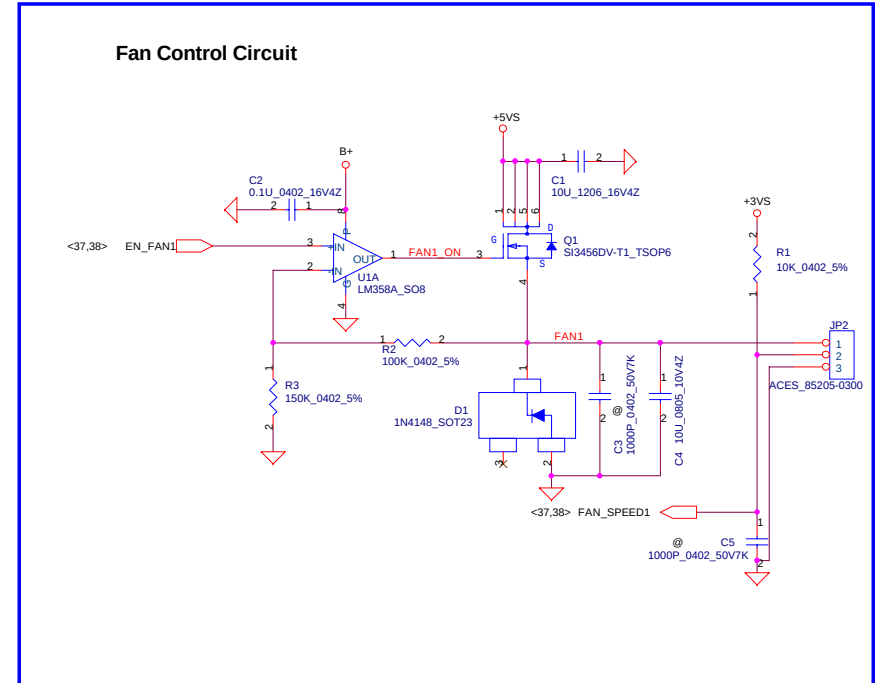
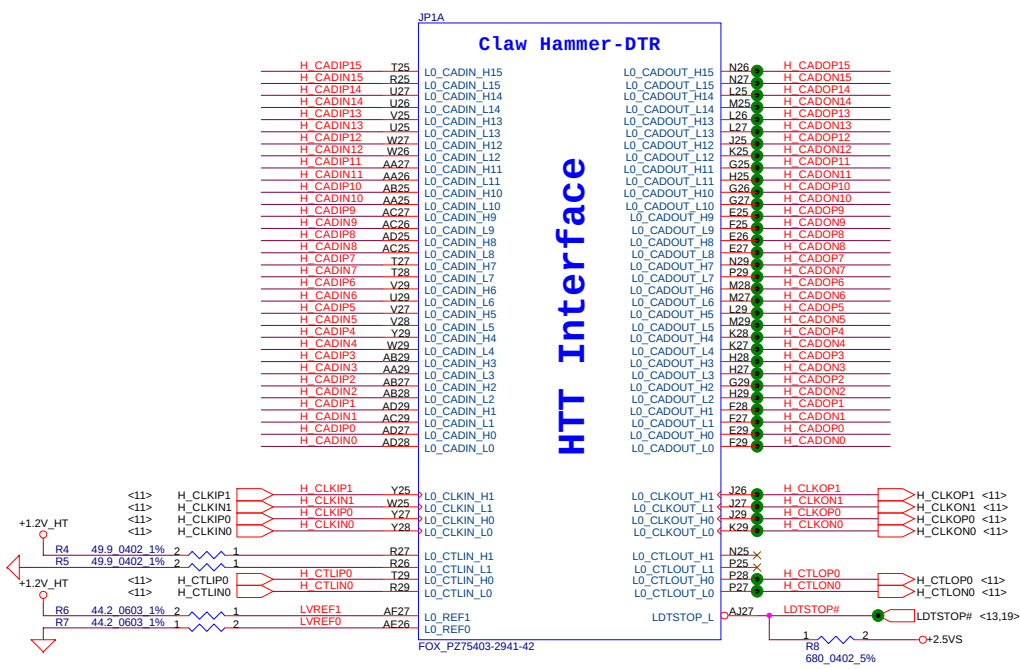
HAL10 17" VRAM@ , SAMSUNG@, HYNIX@, 2HDD@ ,7411@ ,EXP@ ,17_EXP@ ,CIR@ ,D@ ,DOCK@,WL_LED@

HAL20 FF 15.4" VRAM@ , SAMSUNG@, HYNIX@, 2HDD@ ,7411@ ,EXP@ ,15_EXP@ ,CIR@ ,C@ ,DOCK@,WL_LED@, 15.4@(LED)

HAL20 DF 15.4" EXP@, C@ ,DOCK@ ,15.4@(LED), CIR@, WLAN@, 15_EXP@

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H_CADOP[0..15] → H_CADOP[0..15] <11>
H_CADON[0..15] → H_CADON[0..15] <11>



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				Claw Harmmer & Fan			
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50 mil width/20 mil space

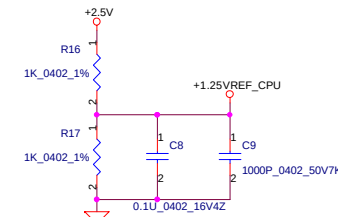
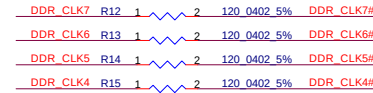
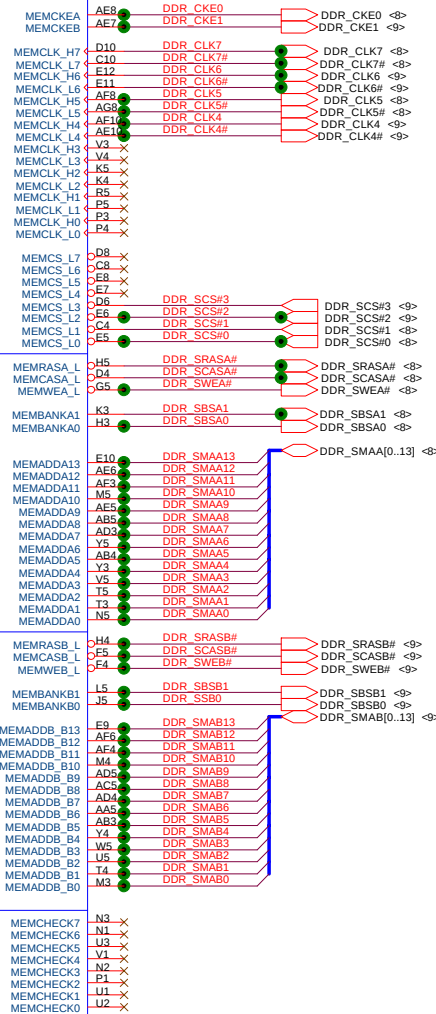


Claw Hammer-DTR

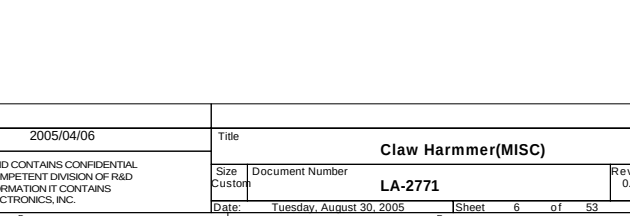
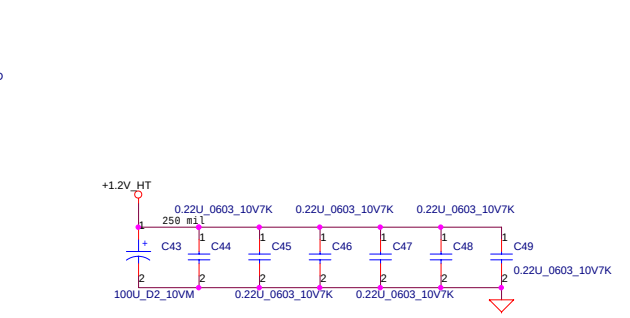
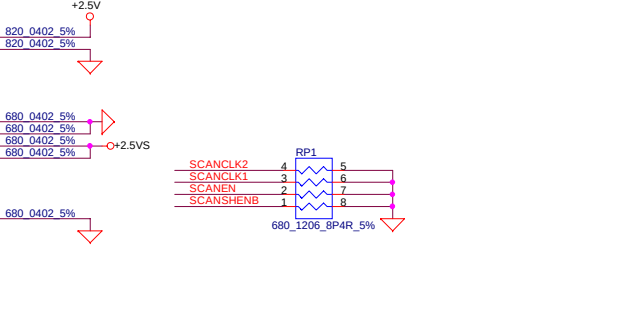
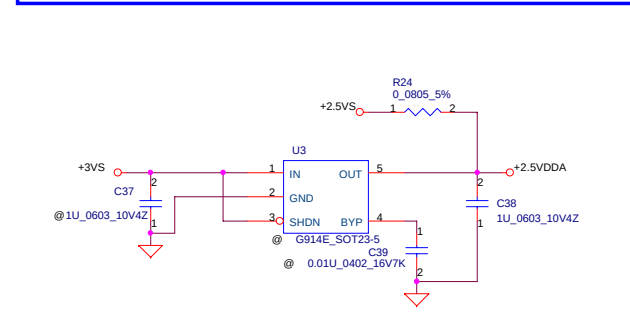
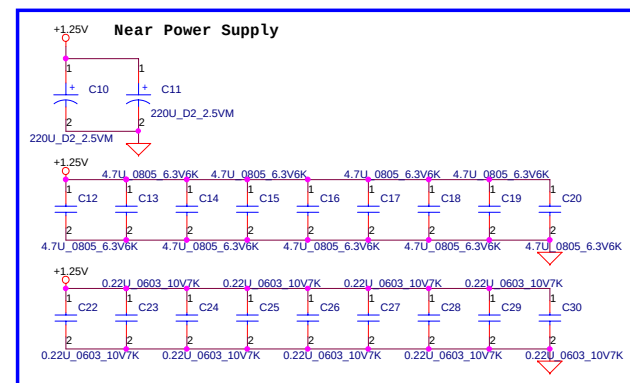
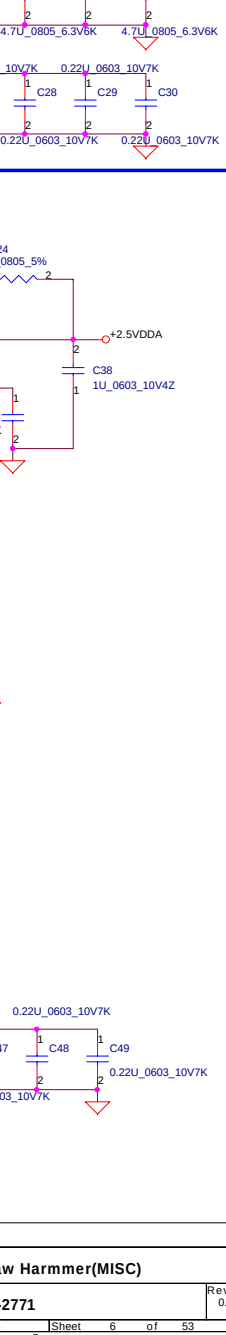
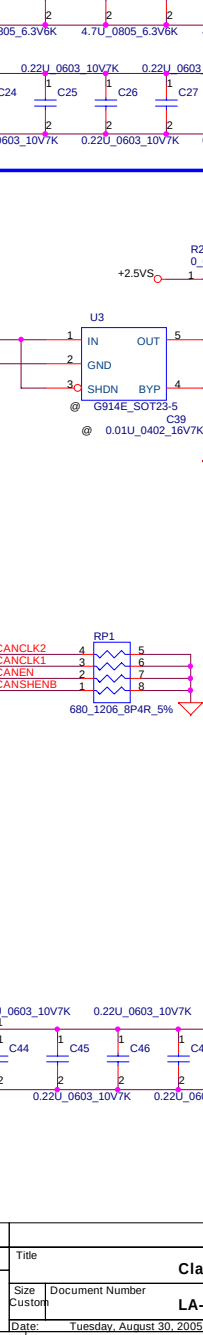
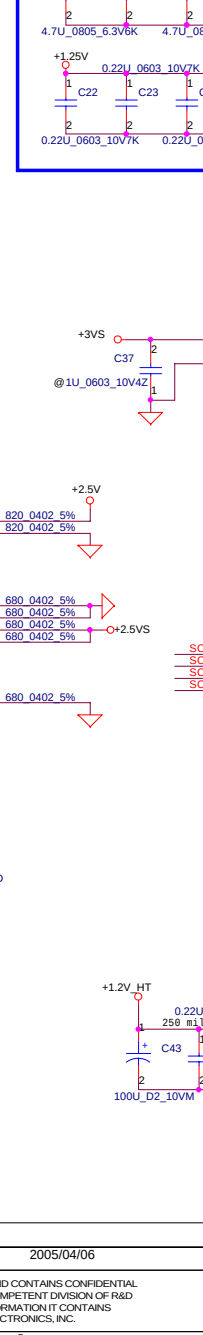
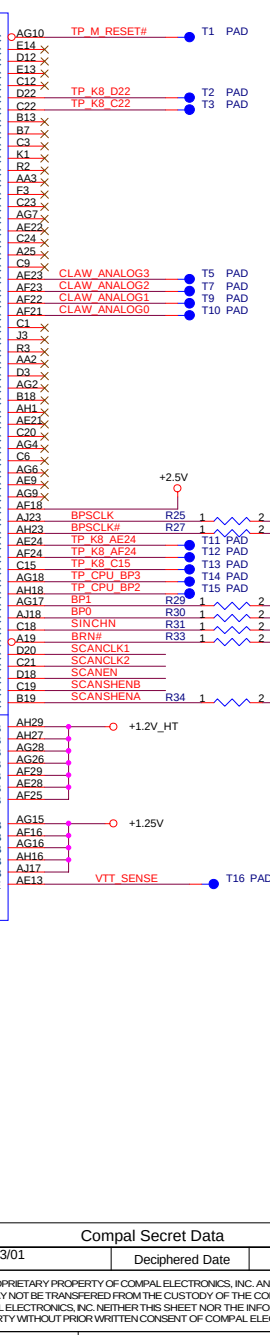
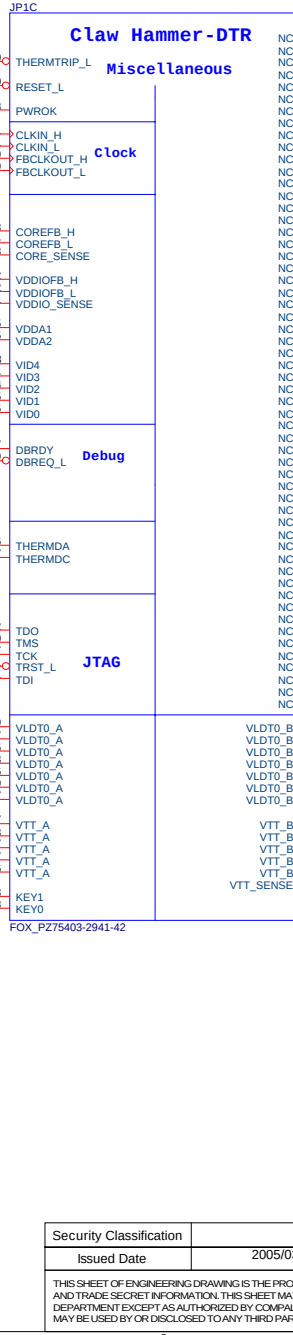
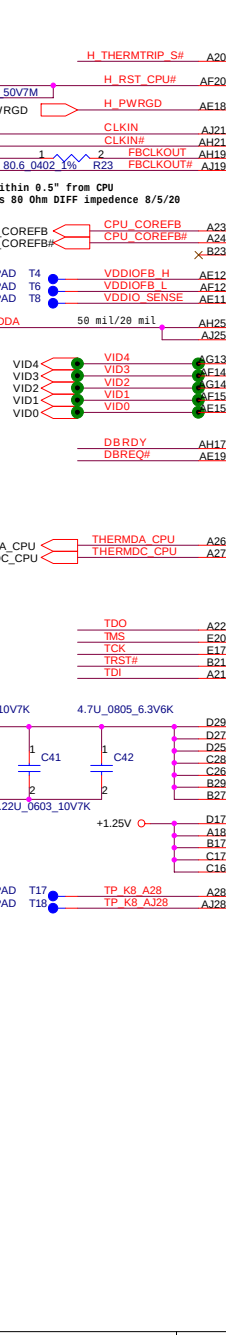
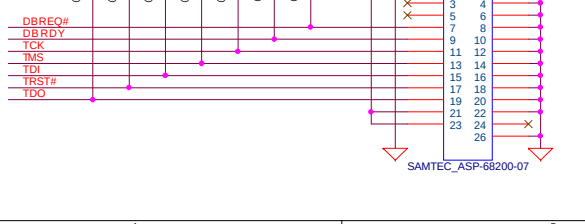
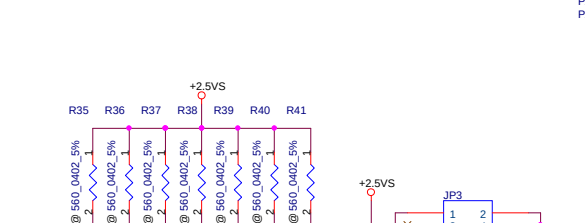
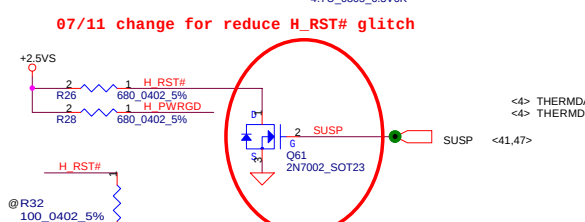
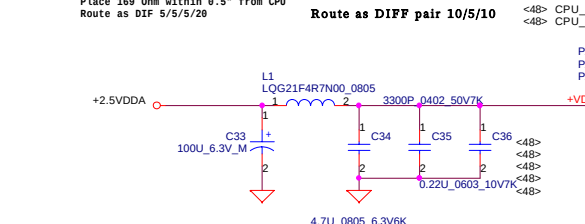
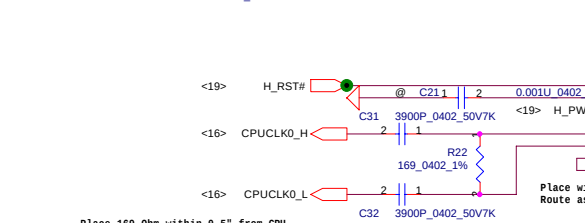
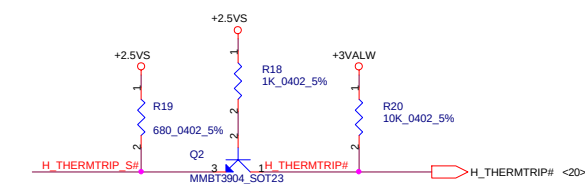
DDR Memory

A CHANGEL ADDRESS

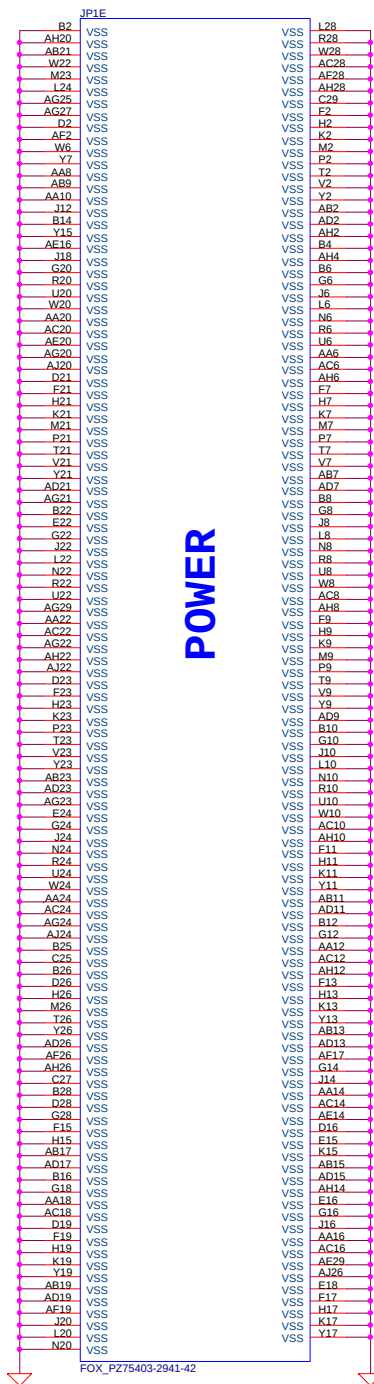
B CHANGEL ADDRESS



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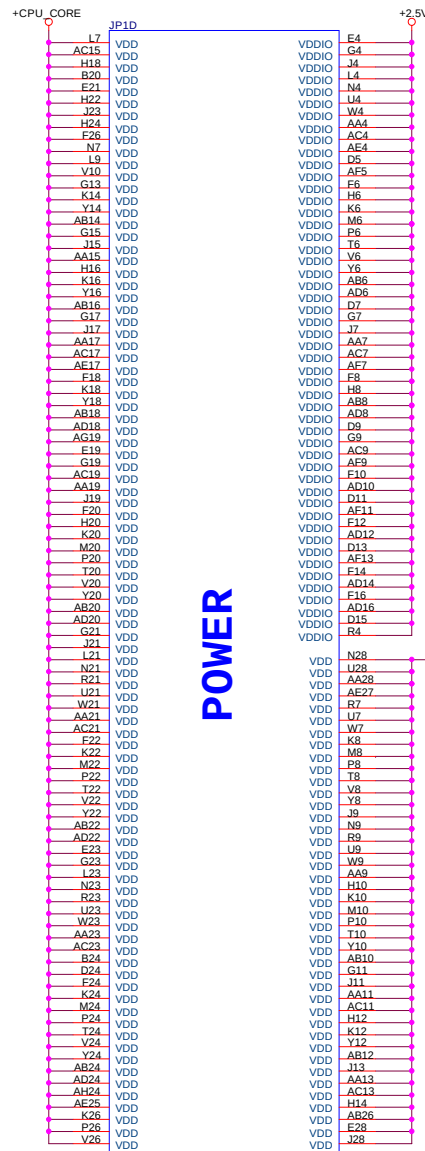


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2005/03/01				2005/04/06				Claw Hammer(MISC)			
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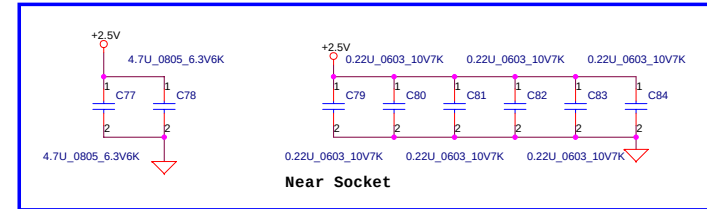
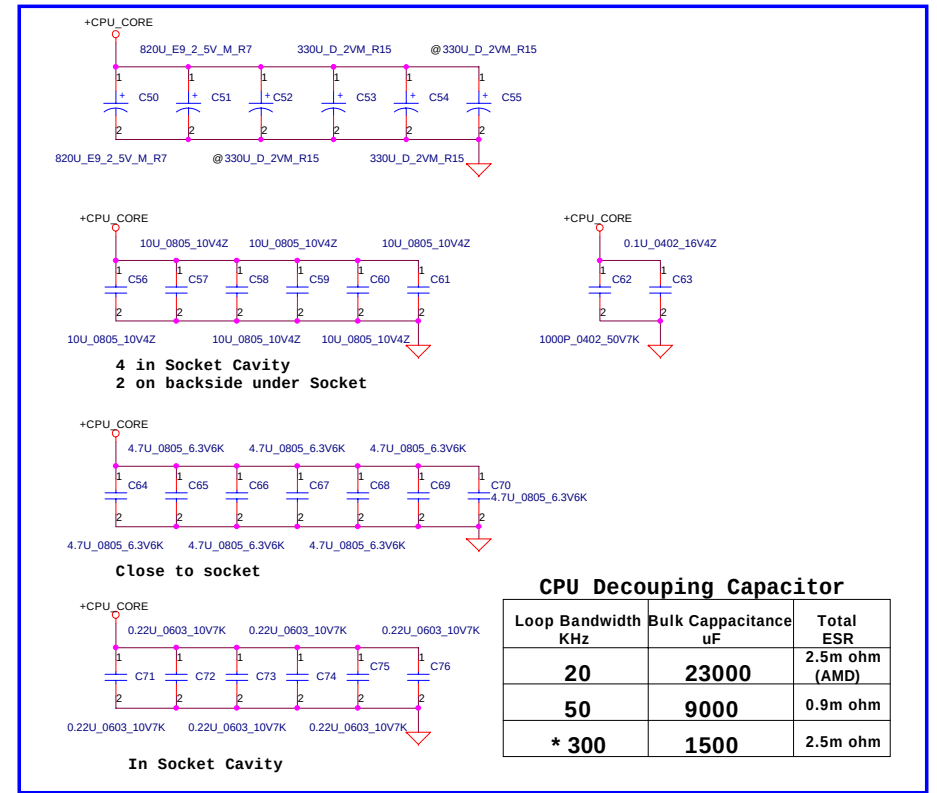
POWER

FOX_PZ75403-2941-42

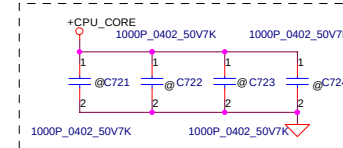


POWER

FOX_PZ75403-2941-42



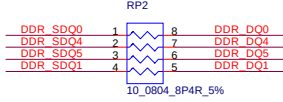
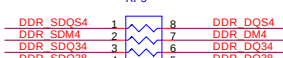
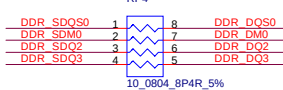
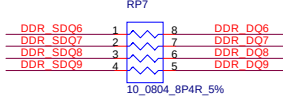
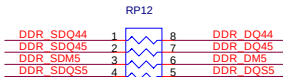
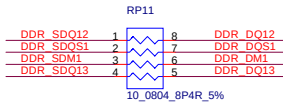
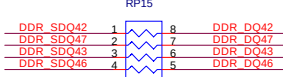
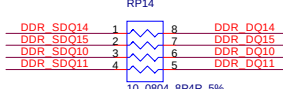
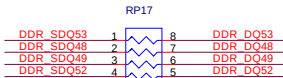
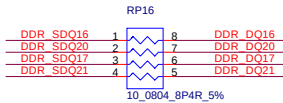
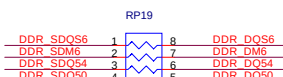
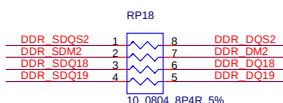
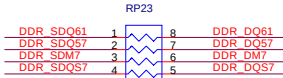
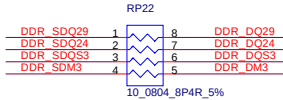
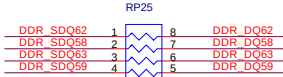
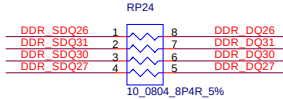
For EMI require



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<> DDR_SDQS[0..7]  DDR_SDQS[0..7]
<> DDR_SDQ[0..63]  DDR_SDQ[0..63]
<> DDR_SDM[0..7]  DDR_SDM[0..7]
<> DDR_SMAA[0..13]  DDR_SMAA[0..13]

DDR_DQ[0..63]  DDR_DQ[0..63] <>
DDR_DQS[0..7]  DDR_DQS[0..7] <>
DDR_DM[0..7]  DDR_DM[0..7] <>



Note:
DDR_SMAA13 Recommend for AMD

Layout note
Place these resistors
close to DIMM0,
all trace length<500 mil

<> DDR_CKE0  DDR_CKE0
<> DDR_SBSA0  DDR_SBSA0
<> DDR_SWEA#  DDR_SWEA#
<> DDR_SCS#0  DDR_SCS#0

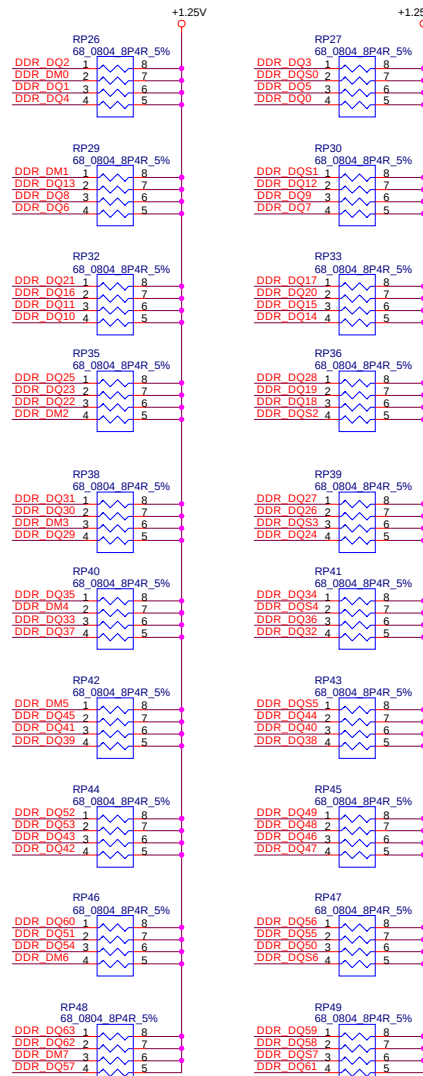
<9.16,20,28> SB_SDAT
<9.16,20,28> SB_SCLK



SO-DIMM0

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 DDR_DQS[0..7]  **DDR_DQS[0..7]**
 DDR_DQ[0..63]  **DDR_DQ[0..63]**
 DDR_DM[0..7]  **DDR_DM[0..7]**
<S> DDR_SMAB[0..13]  **DDR_SMAB[0..13]**



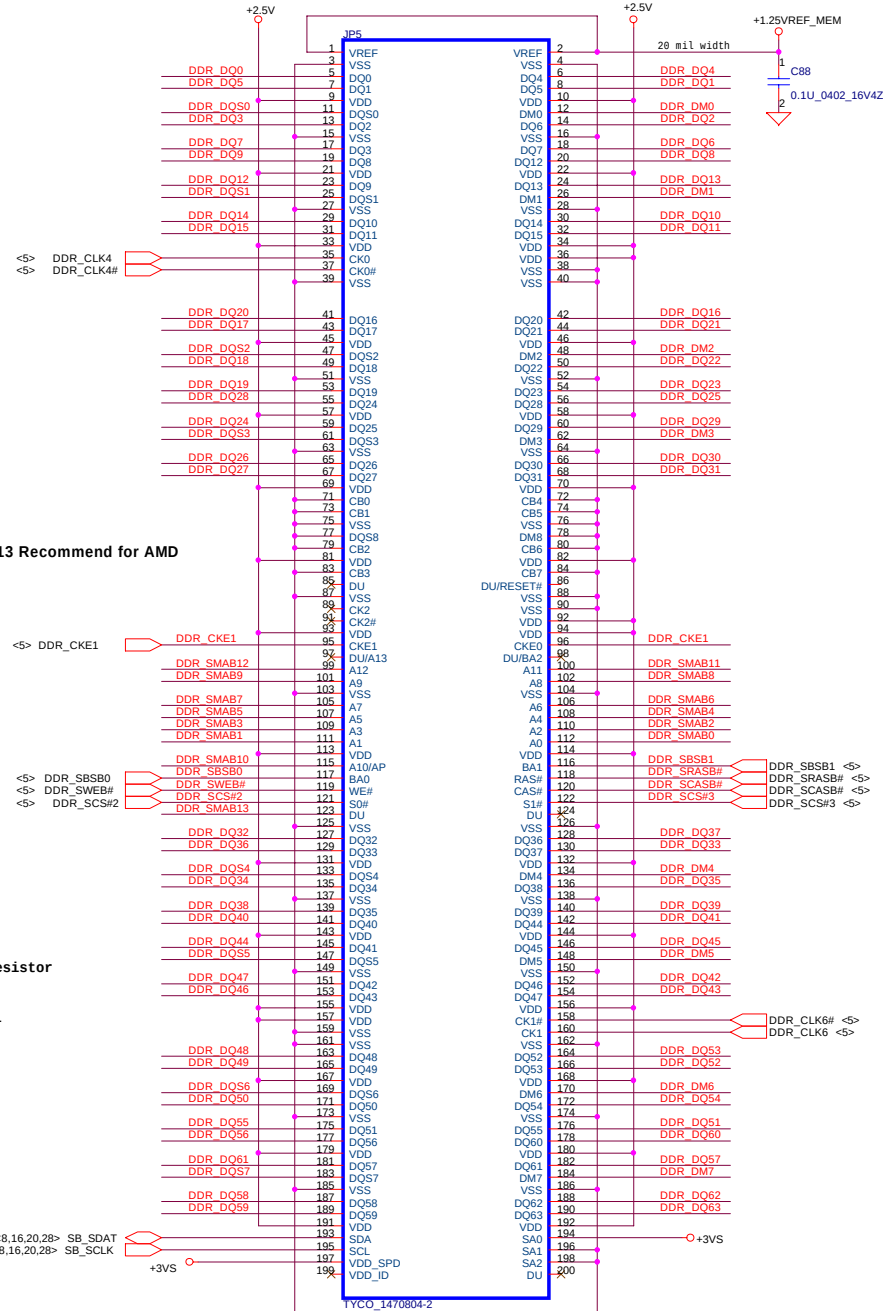
Note:
DDR_SMAA13 Recommend for AMD

<S> DDR_CKE1  **DDR_CKE1**

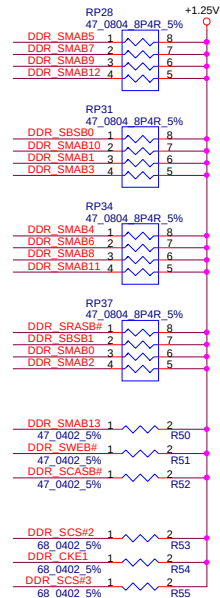
<S> DDR_SBSB0  **DDR_SBSB0**
<S> DDR_SWEB#  **DDR_SWEB#**
<S> DDR_SCS#2  **DDR_SCS#2**

Layout note
Place these resistor
closely DIMM1,
all trace
length<=800mil

<B,16,20,28> SB_SDAT  **SB_SDAT**
<B,16,20,28> SB_SCLK  **SB_SCLK**



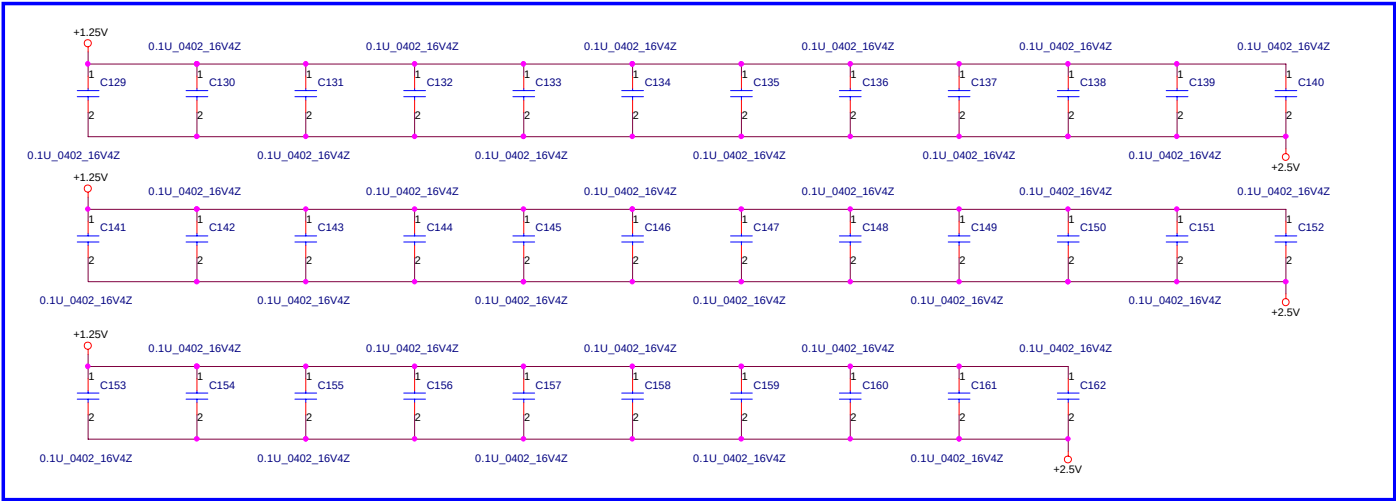
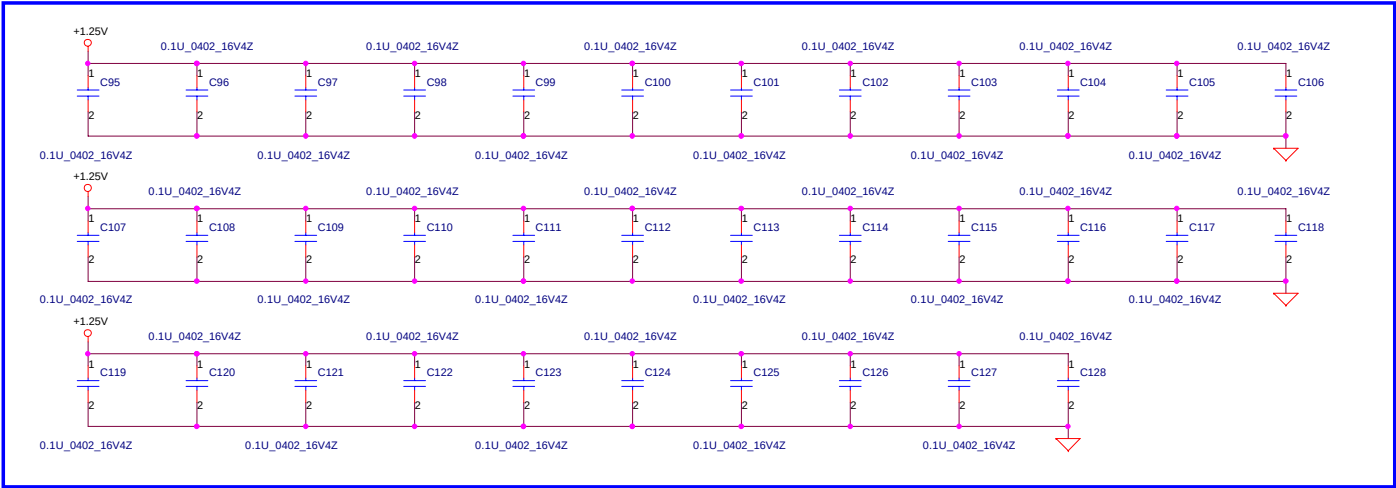
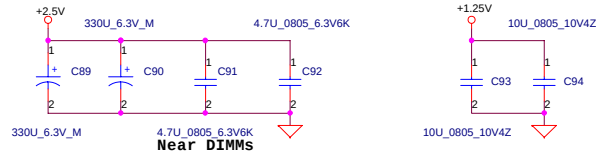
DIMM1



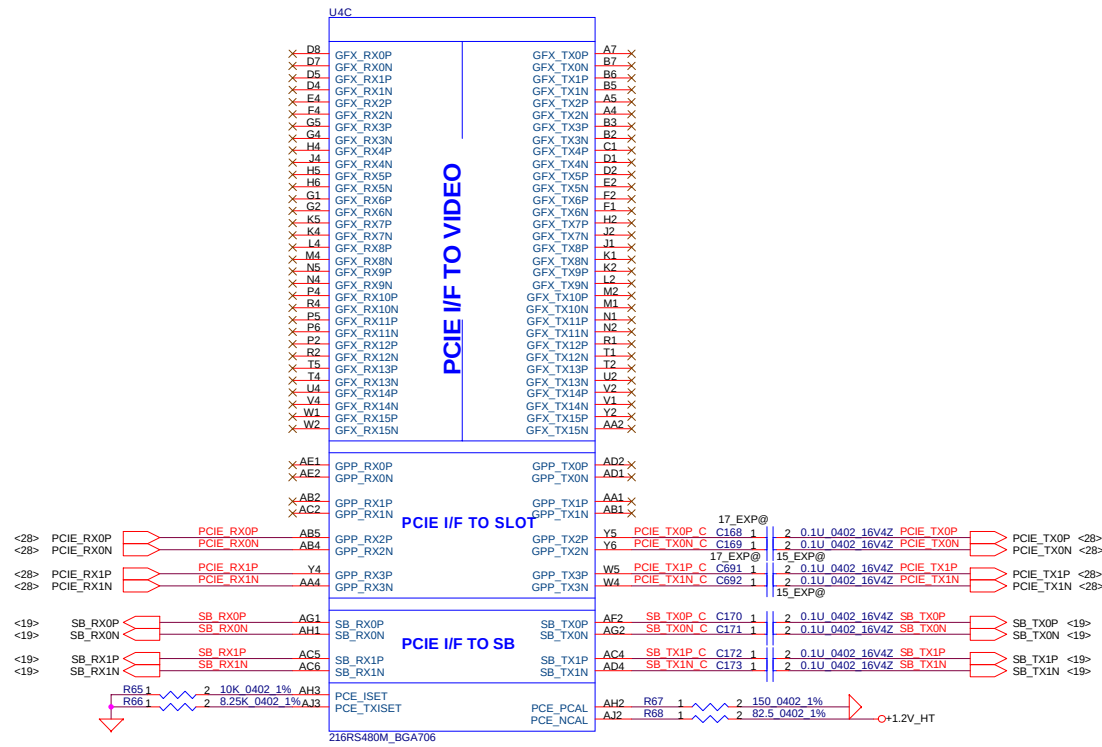
Layout note
Place these resistor
close by DIMM1,
all trace length
Max=0.8"

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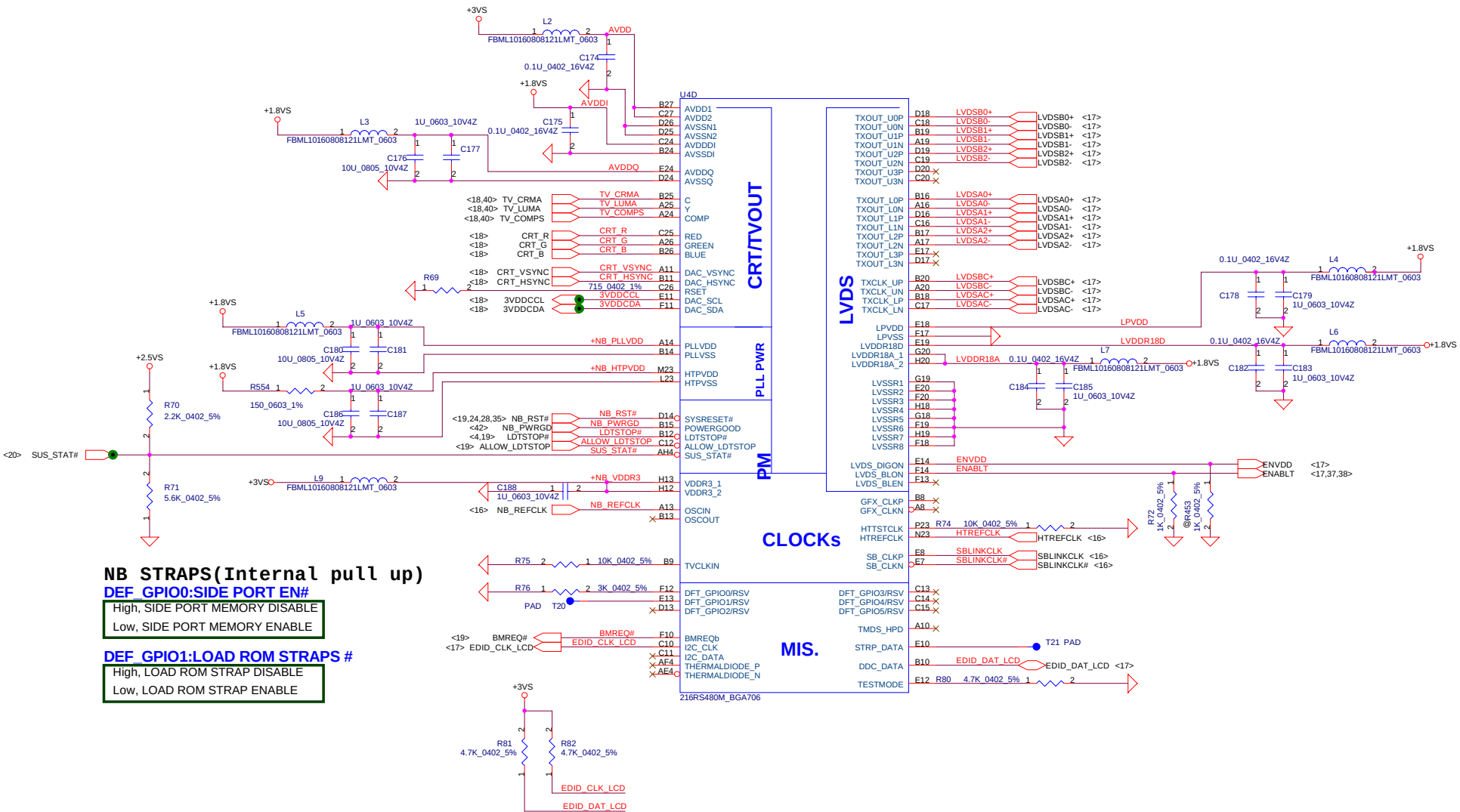
Layout note :
Place one cap close to every 2 pull up resistors termination to +1.25V

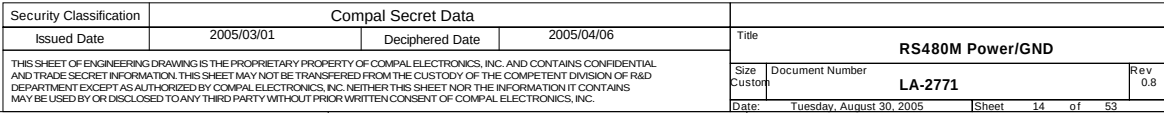


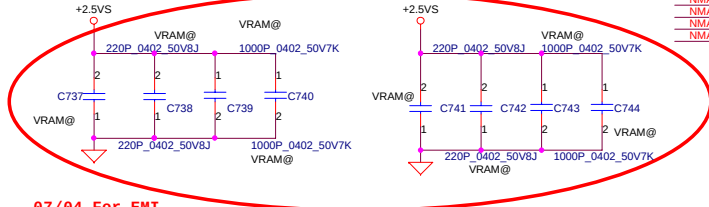
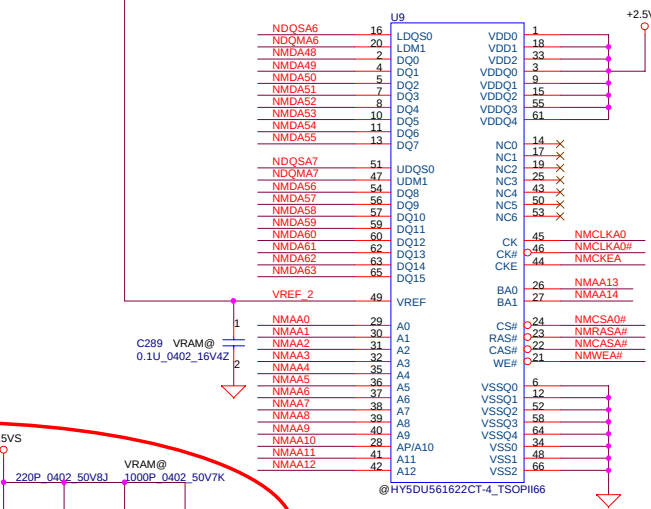
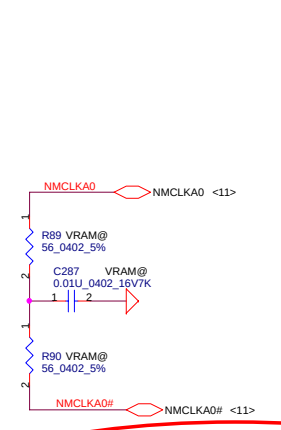
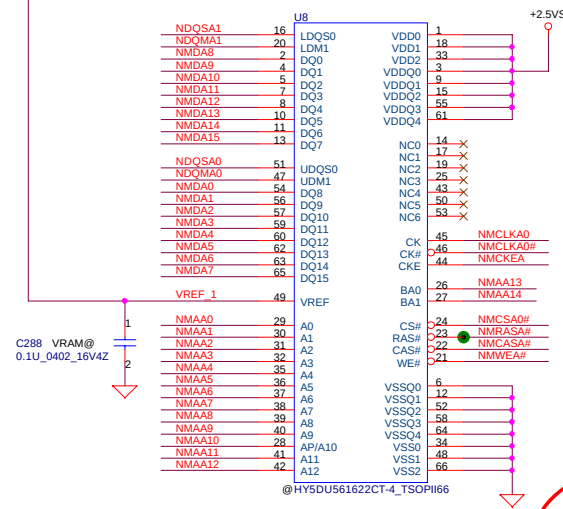
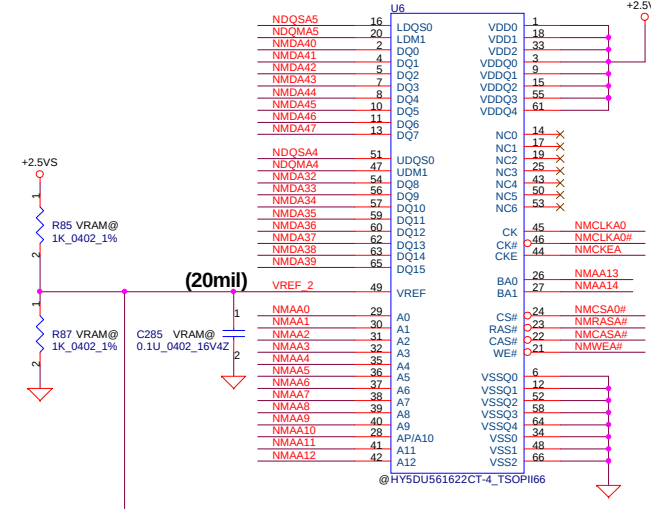
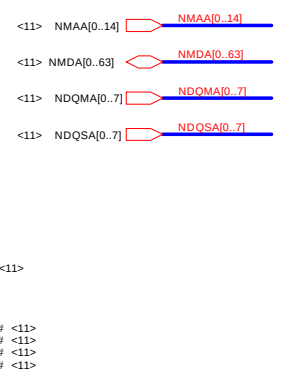
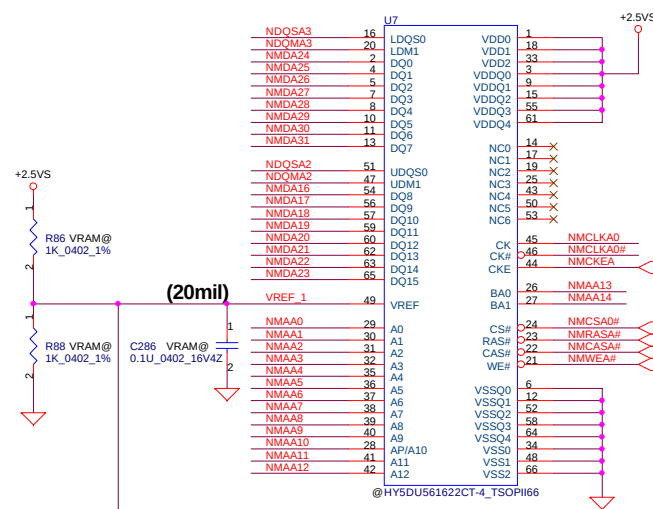
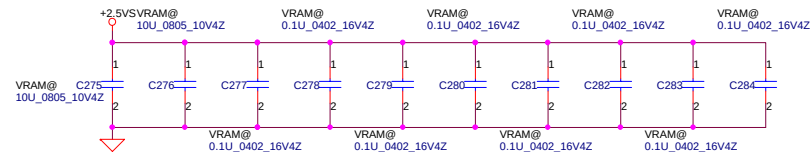
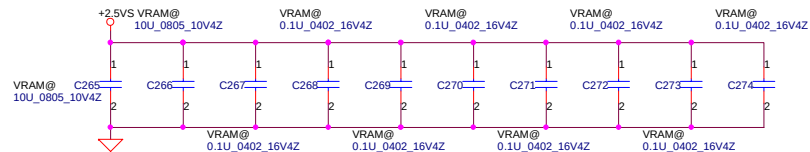
Security Classification		Compal Secret Data			Title		
Issued Date		2005/03/01	Deciphered Date	2005/04/06	DDR Decoupling		
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AVDD , AVDDI , AVDDQ , +NB_PLLVDD , +NB_HTPVDD
+NB_VDDR3 , LPVDD , LVDDR18D , LVDDR18A (20mils)

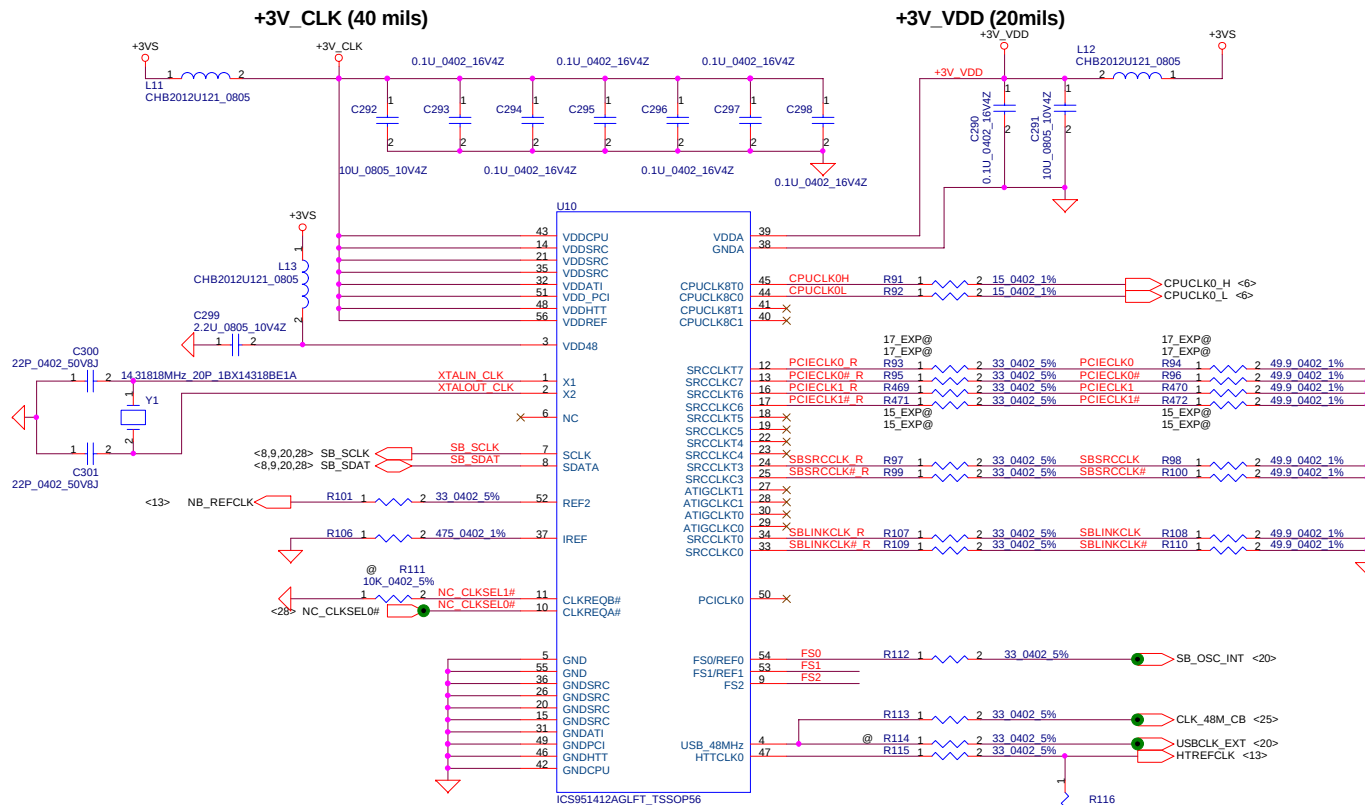






07/04 For EMI

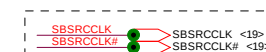
Security Classification		Compal Secret Data				
Issued Date	2005/03/01	Deciphered Date	2005/04/06	Title	VGA DDR	
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				Custom	LA-2771	0.8
Date: Tuesday, August 30, 2005		Sheet 15 of 53				



Express Card(17)



Express Card(15.4)



A link Express



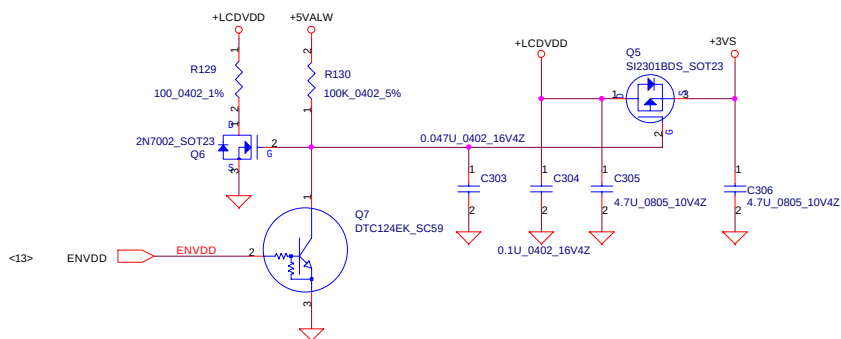
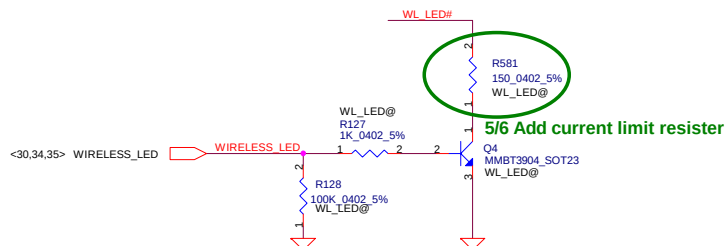
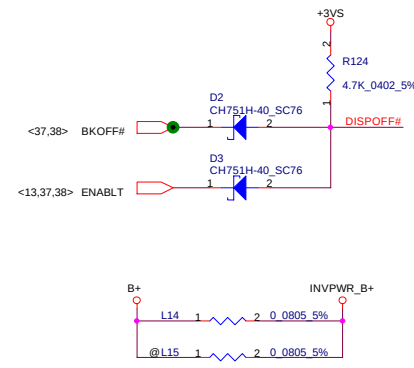
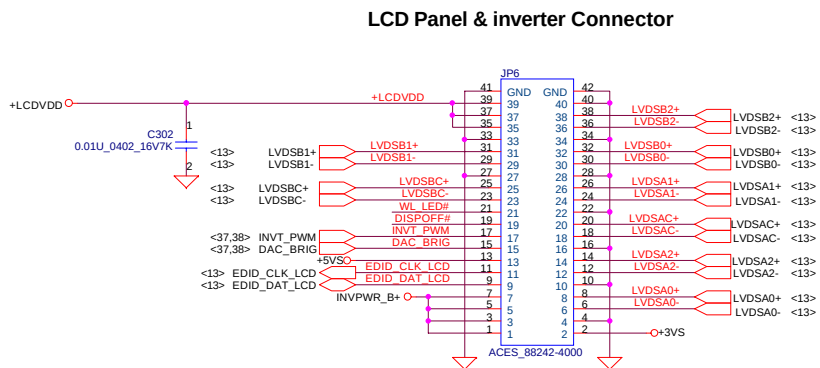
A link Express

EXT CLK FREQUENCY SELECT TABLE(MHZ)

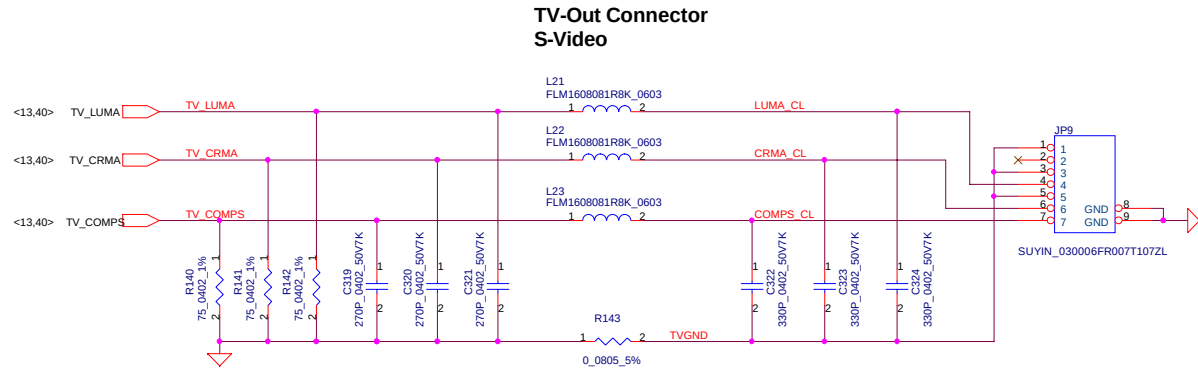
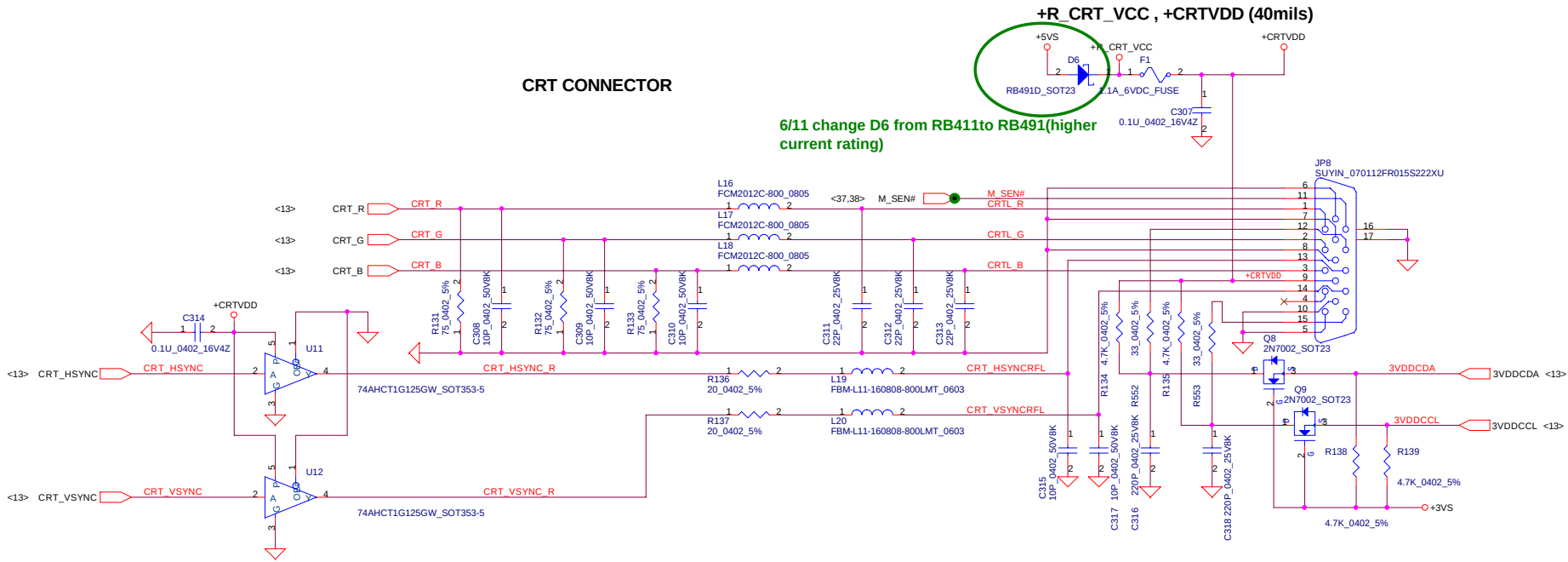
FS2	FS1	FS0	CPU	SRCCLK [2:1]	HTT	PCI	USB	COMMENT
0	0	0	Hi-Z	100.00	Hi-Z	Hi-Z	48.00	Reserved
0	0	1	X	100.00	X/3	X/6	48.00	Reserved
0	1	0	180.00	100.00	60.00	30.00	48.00	Reserved
0	1	1	220.00	100.00	36.56	73.12	48.00	Reserved
1	0	0	100.00	100.00	66.66	33.33	48.00	Reserved
1	0	1	133.33	100.00	66.66	33.33	48.00	Reserved
1	1	1	200.00	100.00	66.66	33.33	48.00	Normal HAMMER operation

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								Size Custom	Document Number	LA-2771		Rev	0.8
								Date:	Tuesday, August 30, 2005	Sheet	16	of	53

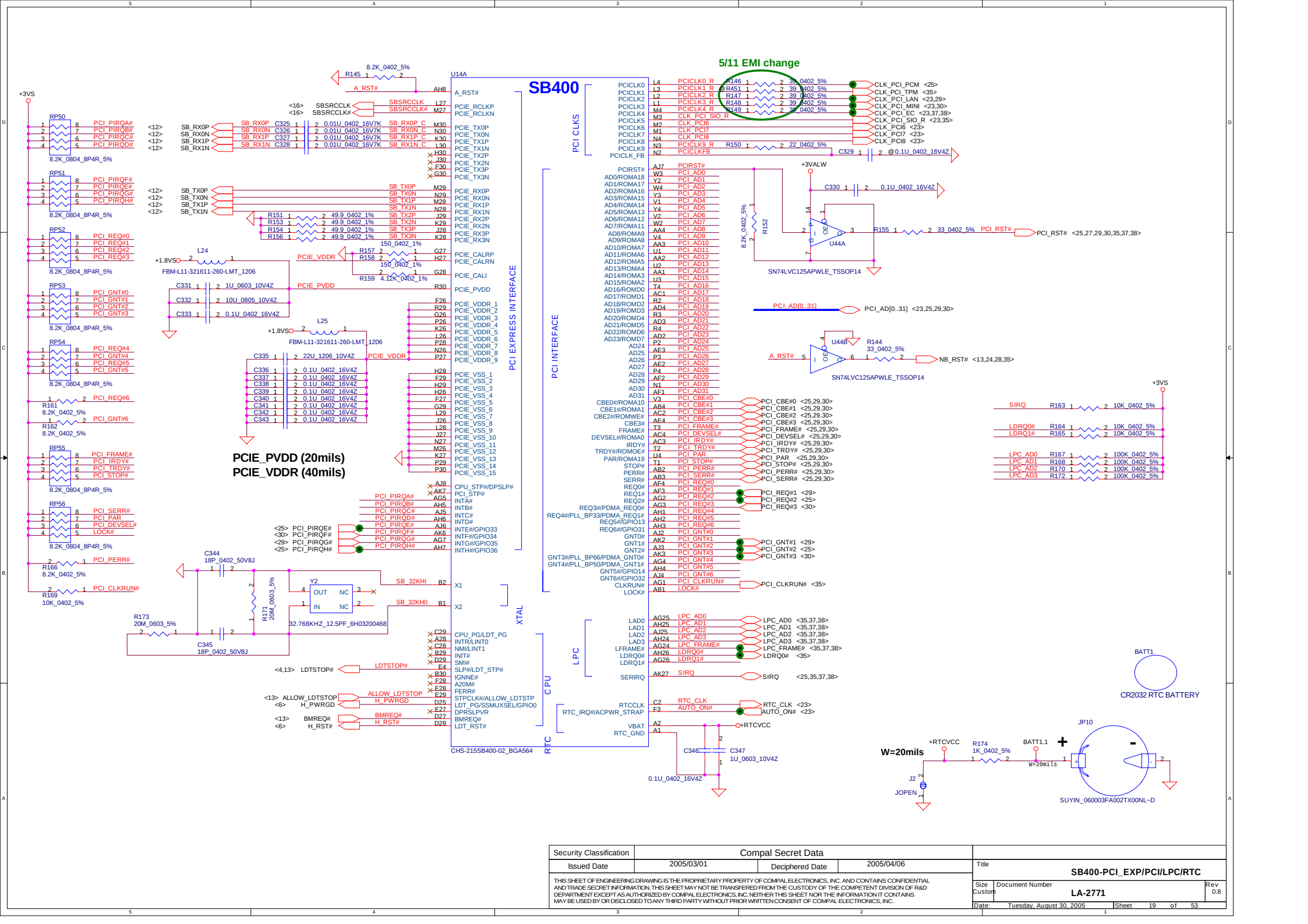
LCD Panel & inverter Connector



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Issued Date		2005/03/01		Deciphered Date		2005/04/06		Title			
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						Size		Document Number		Rev	
						Custom		LA-2771		0.8	
						Date:		Tuesday, August 30, 2005		Sheet 17 of 53	

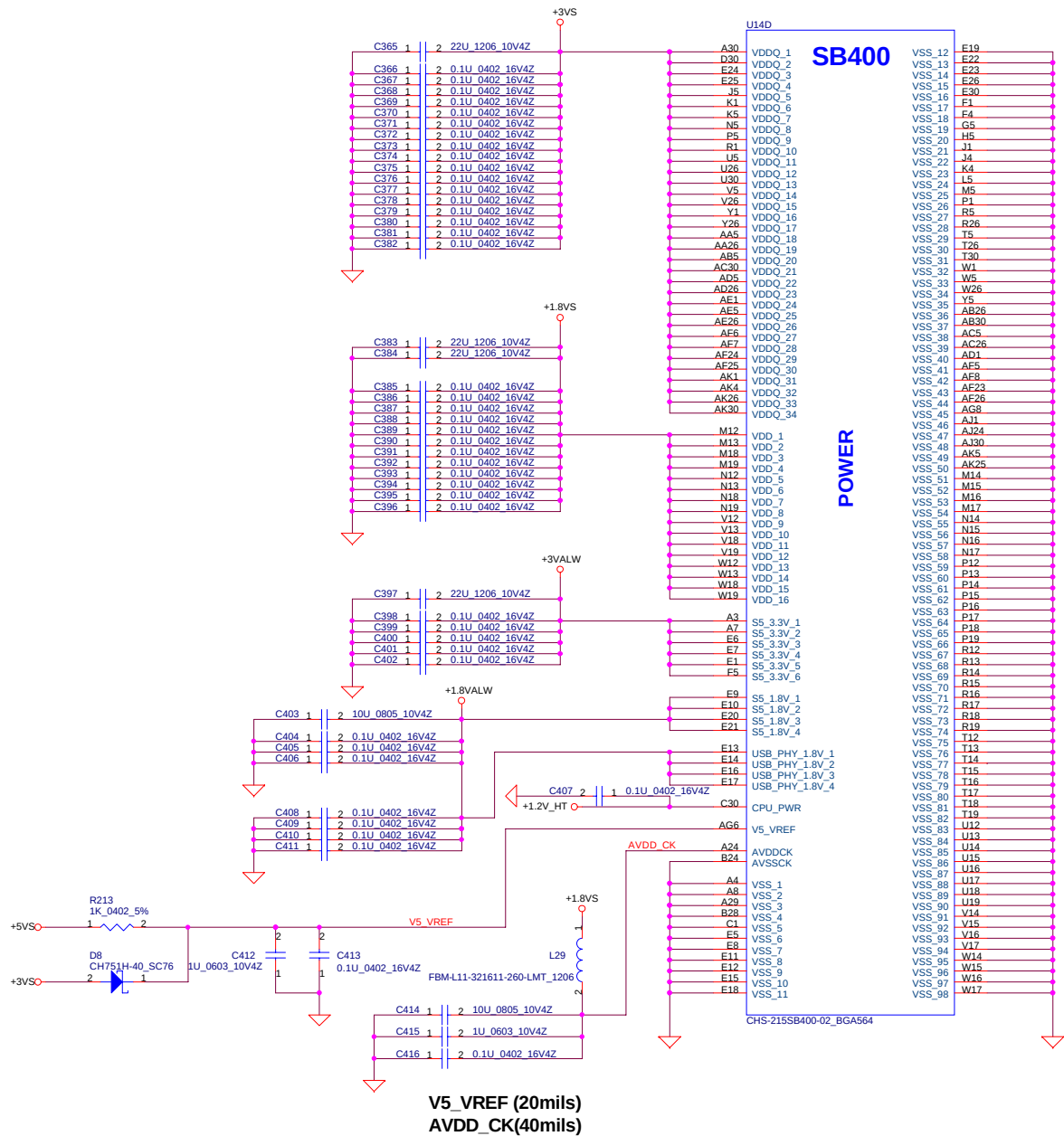


Security Classification		Compal Secret Data		Title	
Issued Date	2005/03/01	Deciphered Date	2005/04/06	CRT Connector & TV-OUT CONN	
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				Date	Sheet 18 of 53

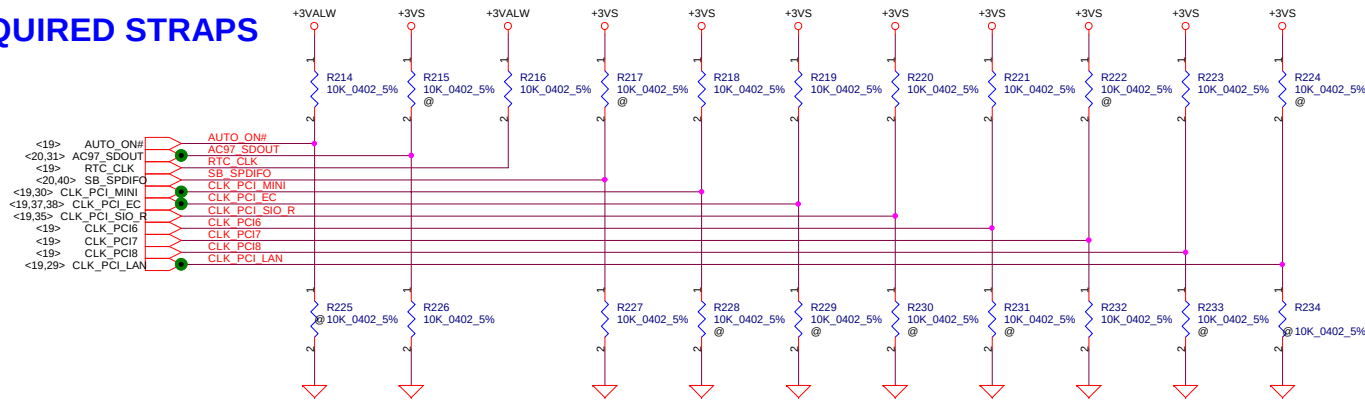


Security Classification		Compal Secret Data		Title SB400-USB/ACPI/AC97/GPIO		
Issued Date	2005/03/01	Deciphered Date	2005/04/06			
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					LA-2771	
				Date:	Tuesday, August 30, 2005	Sheet

Security Classification		Compal Secret Data			Title		
Issued Date	2005/03/01	Deciphered Date	2005/04/06		SB400-IDE/SATA		
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					Date	Tuesday, August 30, 2005	Sheet 21 of 53



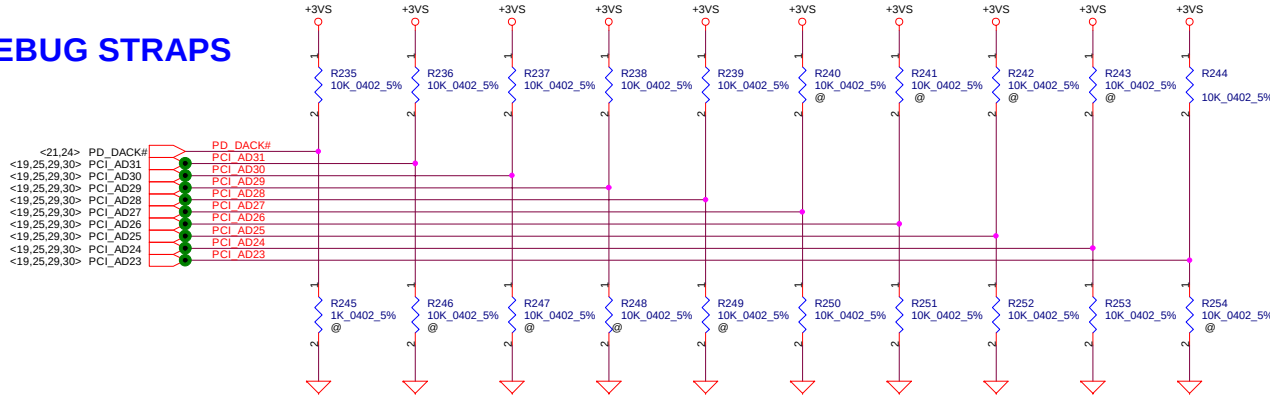
REQUIRED STRAPS



ACPWON

	AUTO_ON#	AC97_SDOOUT	RTC_CLK	SB_SPDIFO	CLK_PCI_LAN	CLK_PCI_MINI	CLK_PCI_EC	CLK_PCI_SIO	CLK_PCI6	CLK_PCI7	CLK_PCI8
PULL HIGH	MANUAL PWR ON DEFAULT	USE DEBUG STRAPS	INTERNAL RTC DEFAULT	SIO 24MHz	48MHz OSC MODE	USB PHY PWRDOWN DISABLE DEFAULT	INTERNAL 48MHz DEFAULT	PCIE_CM_SET LOW DEFAULT	CPU I/F = K8 DEFAULT	ROM TYPE H,H = PCI ROM H,L = PMC LPC ROM	
PULL LOW	AUTO PWR ON	IGNORE DEBUG STRAPS DEFAULT	EXTERNAL RTC (NOT SUPPORTED W/ IT8712)	SIO 48MHz DEFAULT	48MHz XTAL MODE	USB PHY PWRDOWN ENABLE	EXTERNAL 48MHz	PCIE_CM_SET High	CPU I/F = P4	L,H = NORMAL LPC ROM L,L = FWH ROM DEFAULT	

DEBUG STRAPS

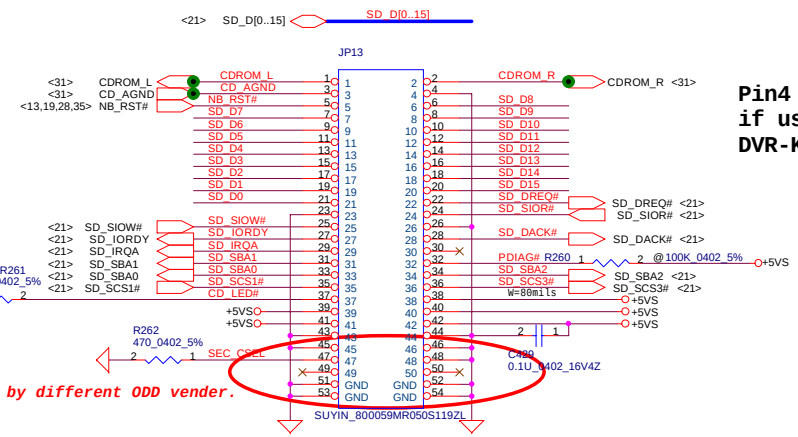
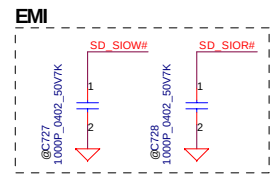
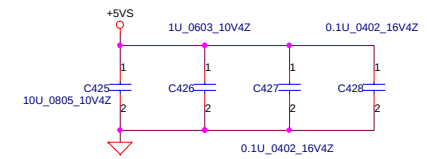
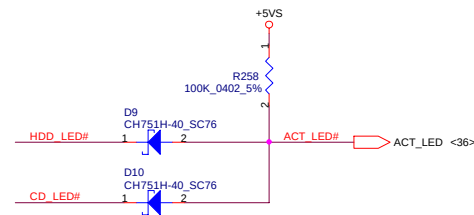


	PD_DACK#	PCI_AD31	PCI_AD30	PCI_AD29	PCI_AD28	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE LONG RESET DEFAULT	RESERVED	RESERVED	RESERVED	RESERVED	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	RESERVED
PULL LOW	USE SHORT RESET					USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	

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Issued Date	2005/03/01	Deciphered Date	2005/04/06	Hardware Trap		
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				Custom	LA-2771	0.8
				Date	Tuesday, August 30, 2005	Sheet 23 of 53

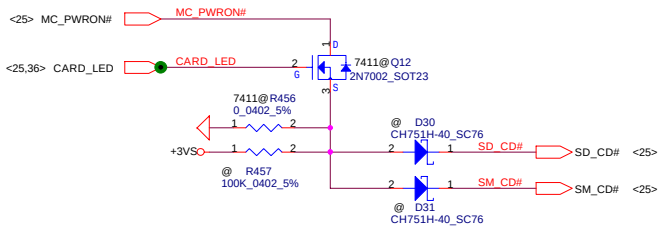
The schematic diagram illustrates the power and signal connections for the SUYIN_200138FR044G2132L board. The top section shows a power supply network with a +5VS input, decoupling capacitors C719 (100uF 4V), C417, C418, C419, and C420, and voltage regulators 10U_0805_10V4Z and 01U_0402_16V4Z. The bottom section shows a pin header JP11 with connections for NB_RST#, PD_D7-D15, PD_DREQ#, PD_IOW#, PD_IORDY, PD_DACK#, PD_IRQA, PD_A1-A0, PD_CS#1, HDD_LED#, +5VS, and GNDGND. It also shows connections for PD_D8-D15, PDIA#G, PD_A2, PD_CS#3, and +5VS. A resistor R255 (470 Ohms) is connected between pins 1 and 2 of a header.

The schematic diagram illustrates the power and signal connections for the SUYIN_200138FR04G2132ZL board. The top section shows a power distribution network starting from a +5VS input, passing through a C720 capacitor, and then through four 2HDD@10U_0805_10V4Z capacitors connected to a 2HDD@0.1U_0402_16V4Z capacitor. The bottom section shows a pin header JP12 with connections for NB_RST#, PD_D7 through PD_D0, PD_FREQ#, PD_IOW#, PD_IOR#, PD_IORDY, PD_DACK#, PD_IRQA, PD_A1 through PD_A0, PD_CS#1, HDD_LED#, +5VS, and GNDGND. It also shows connections for PD_D8 through PD_D15, PCSEL_S, PDIAAG, PD_A2, PD_CS#3, and +5VS.

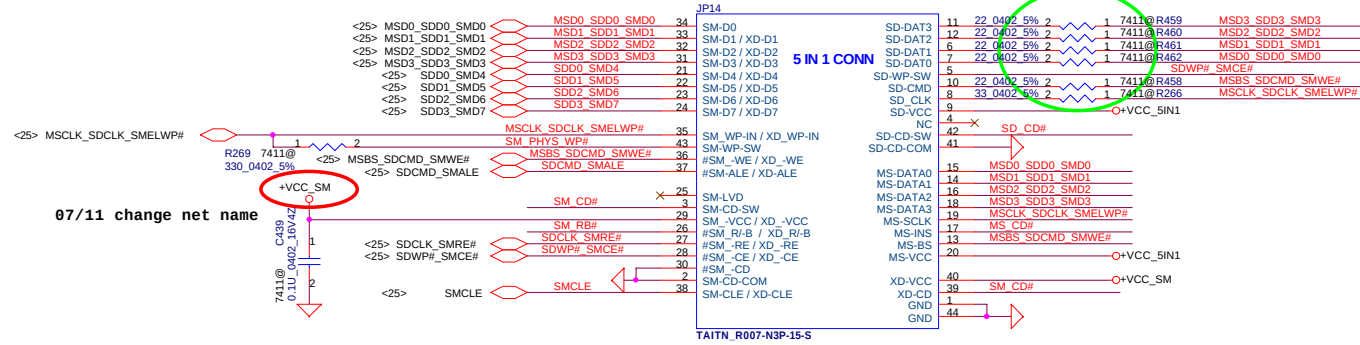


07/11 for defined by different ODD vender.

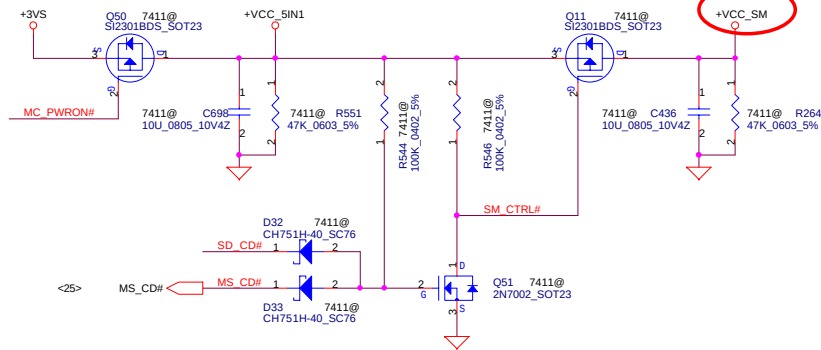
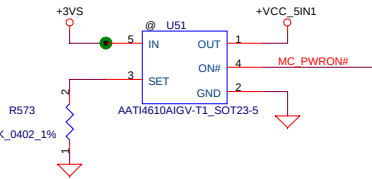
Security Classification	Compal Secret Data						
Issued Date	2005/03/01	Deciphered Date	2005/04/06	Title	HDD/CDROM		
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				Custom	LA-2771	0.8	
				Date:	Tuesday, August 30, 2005	Sheet	24 of 53



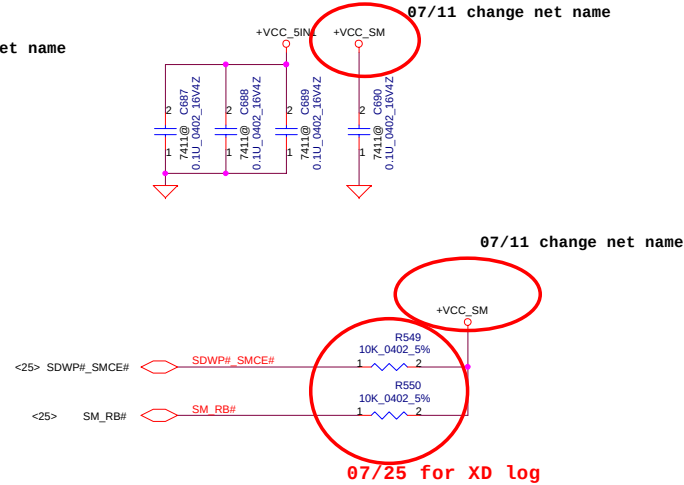
8/23 Relocate Damping resistor to solve Sandisk Card issue



07/11 change net name



07/11 change net name



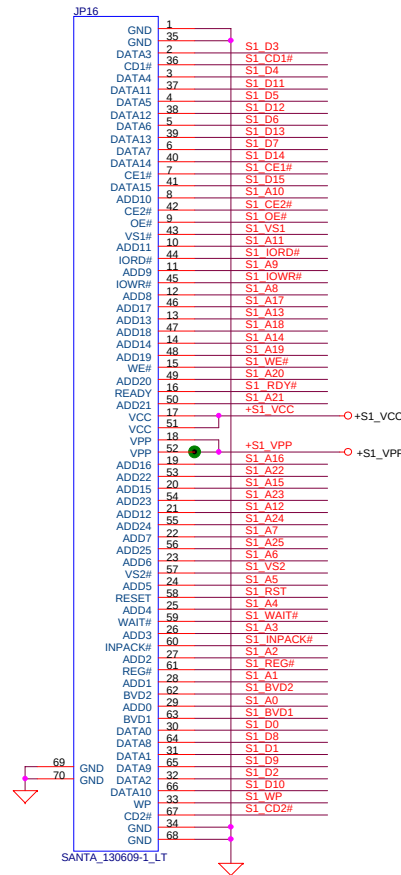
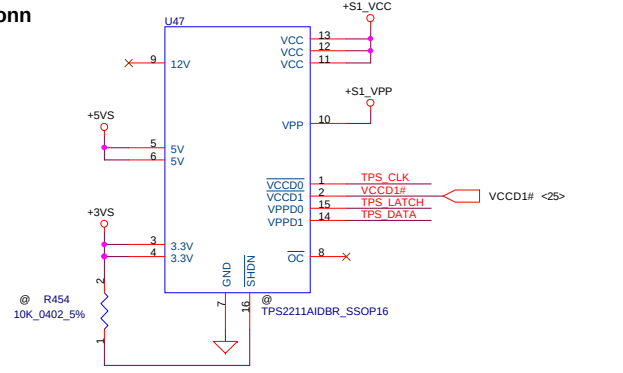
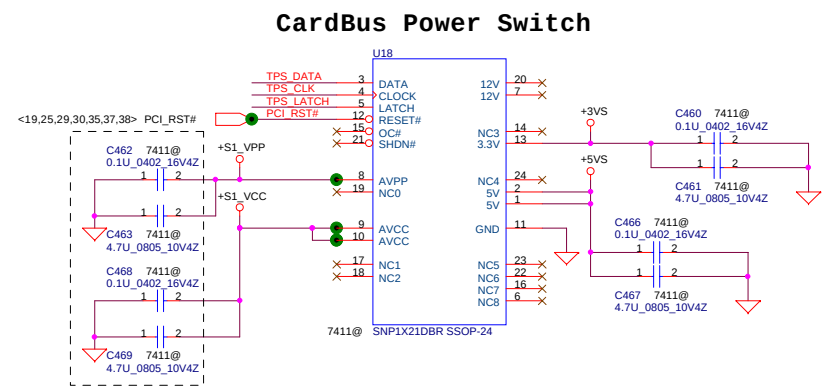
07/11 change net name

07/11 change net name

07/25 for XD log

Security Classification				Compal Secret Data				Title			
Issued Date				2005/03/01				Deciphered Date			
								2005/04/06			
								PC17411(2/3)			
								LA-2771			
								Rev 0.8			
								Date: Tuesday, August 30, 2005			
								Sheet 26 of 53			

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				Custom	
				Rev	0.8

The diagram illustrates the J20 connector for the TPS2231P WPU. The connector pins are numbered 1 through 24. The signals connected to the pins are as follows:

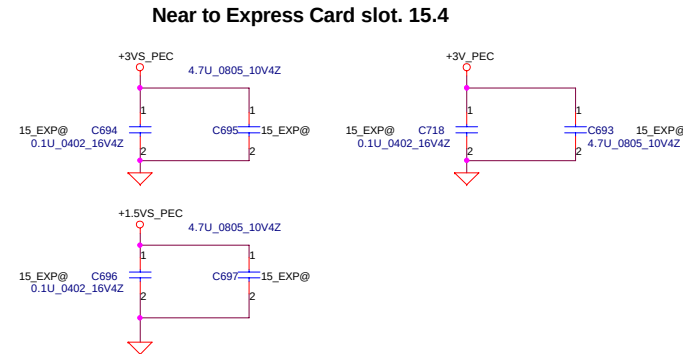
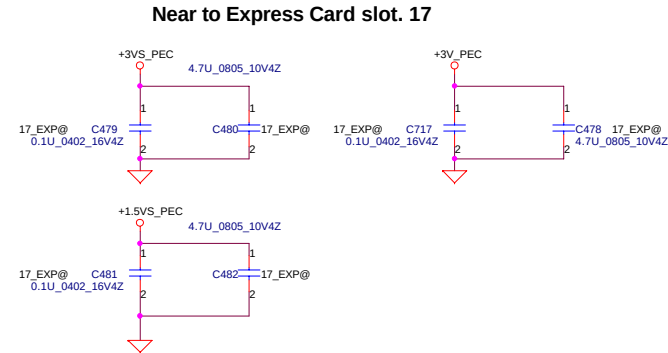
- Pins 1-4: EXP@ C472, EXP@ C474, EXP@ C476, and GND.
- Pins 5-8: +3VS, +3V_PEC, +3VALW, and +1.5VS.
- Pins 9-12: +1.5V_PEC, EXP@ R302, NC_CLKSEL0#, and EXP@ Q55.
- Pins 13-16: NC_CP#, SUSP#, SYSON, and NB_RST#.
- Pins 17-20: GND, NC1, NC2, NC3, and NC4.
- Pins 21-24: GND, NC5, NC6, NC7, and NC8.

The TPS2231P WPU is connected to the J20 connector via a 2-pin header. The header pins are numbered 1 and 2. The signals connected to the header pins are:

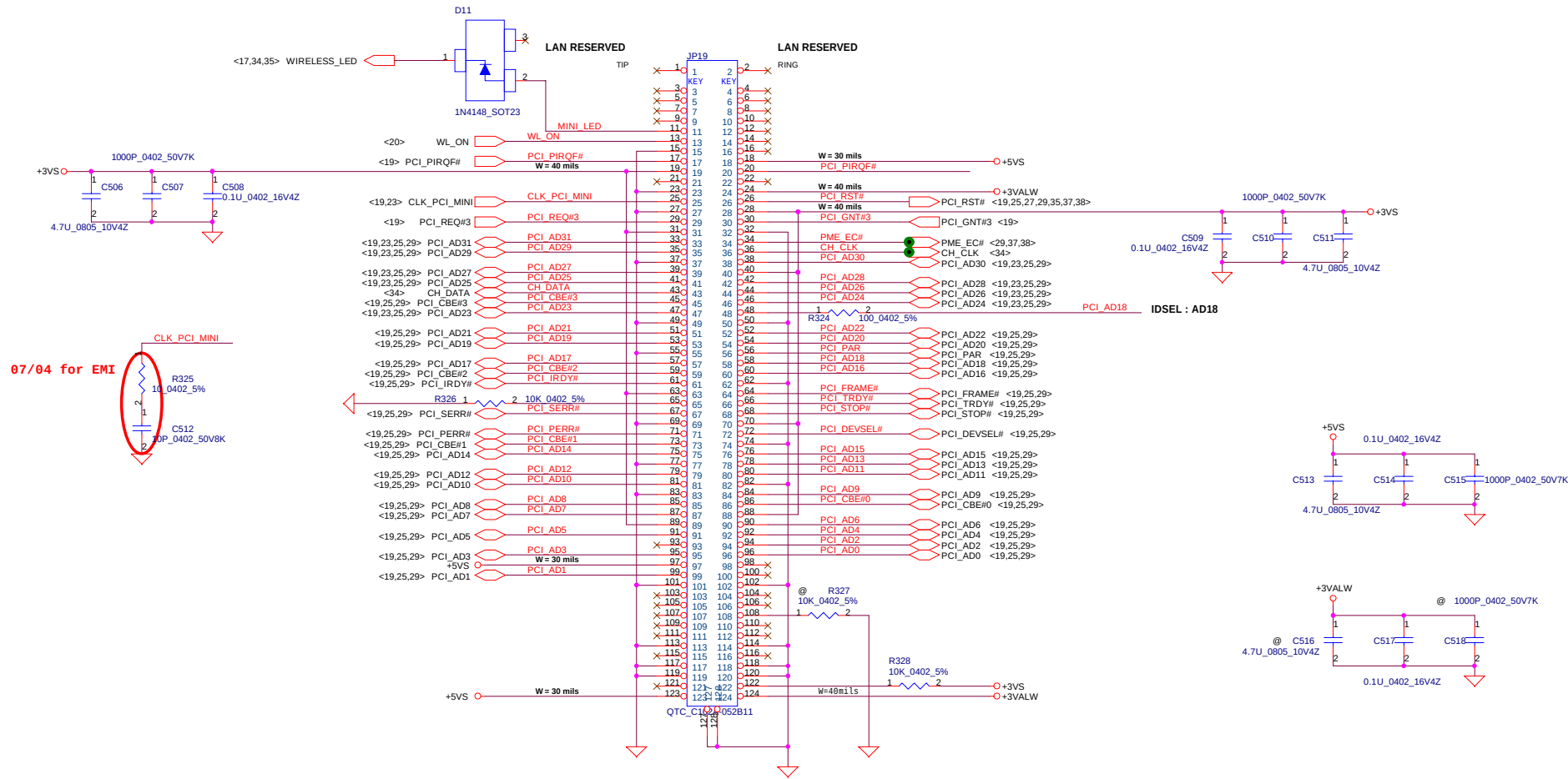
- Pin 1: EXP@ Q55
- Pin 2: EXP@ R302

The diagram also shows the connection of the TPS2231P WPU to the J20 connector via a 2-pin header. The header pins are numbered 1 and 2. The signals connected to the header pins are:

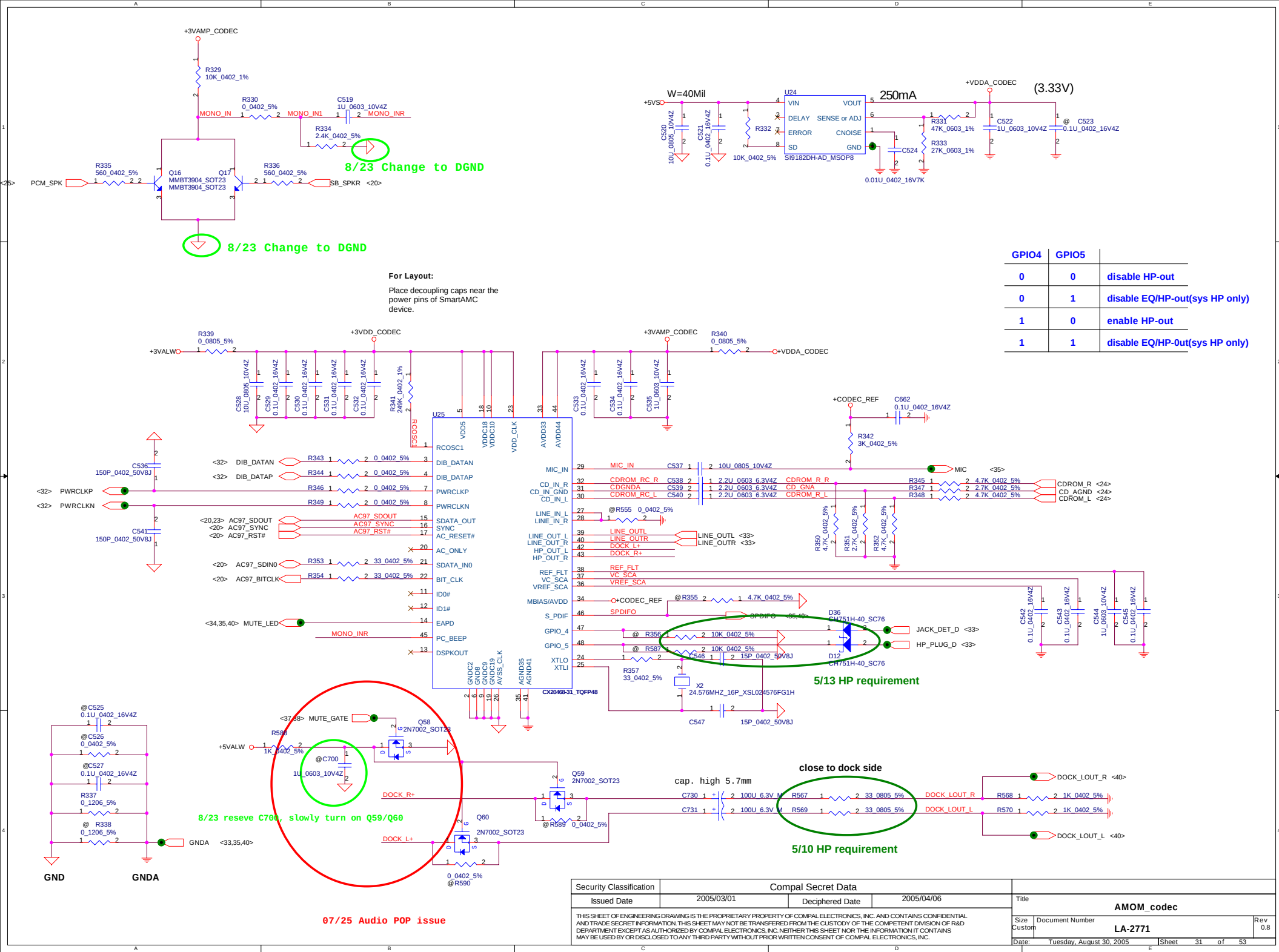
- Pin 1: EXP@ Q55
- Pin 2: EXP@ R302



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				Tuesday, August 30, 2005	0.8
				Sheet	30 of 53



8/23 Change to D6ND

8/23 Change to D6ND

For Layout:
Place decoupling caps near the power pins of SmartAMC device.

GPIO4	GPIO5	
0	0	disable HP-out
0	1	disable EQ/HP-out(sys HP only)
1	0	enable HP-out
1	1	disable EQ/HP-out(sys HP only)

5/13 HP requirement

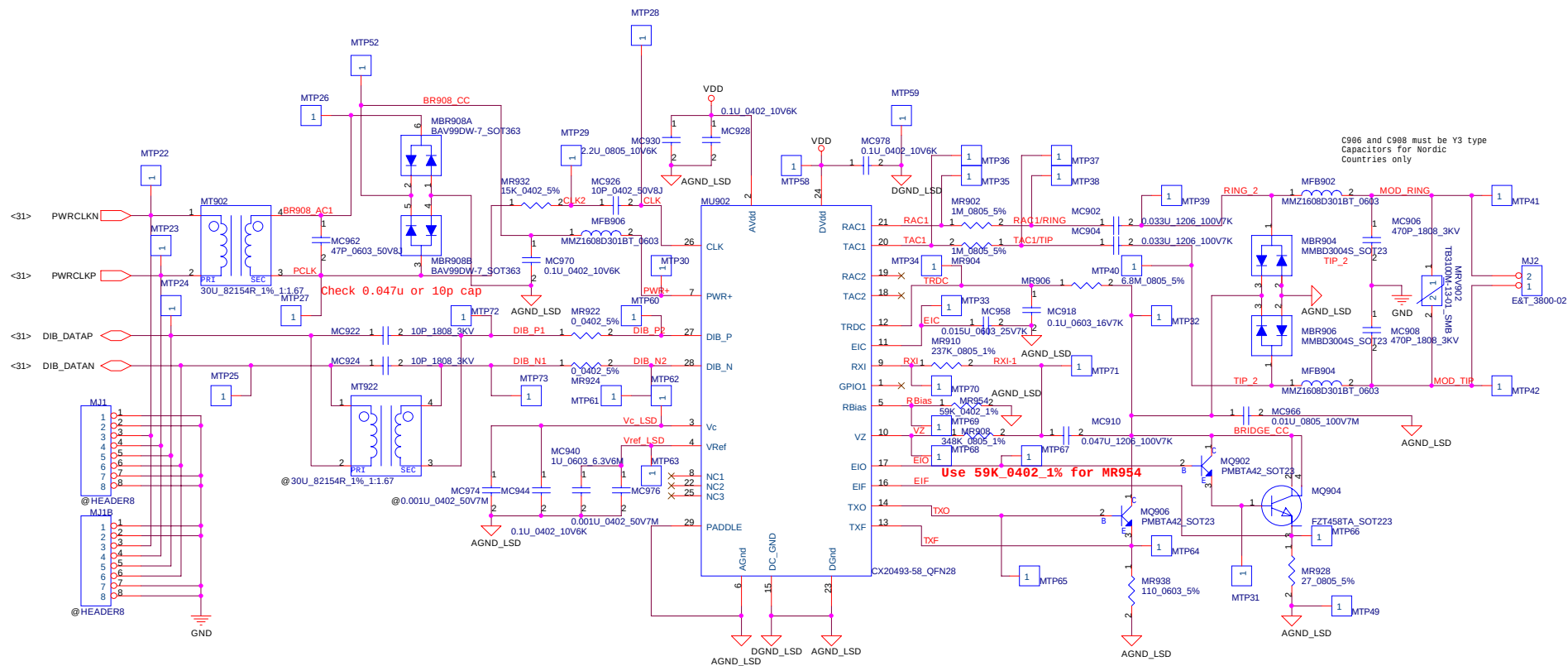
close to dock side

5/10 HP requirement

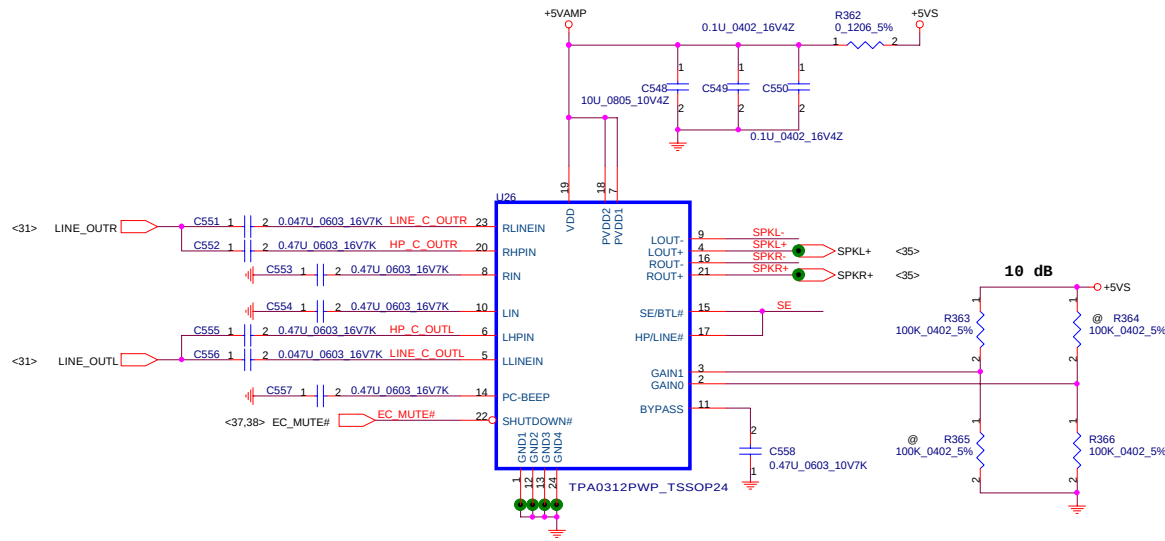
8/23 reseed C700, slowly turn on Q59/Q60

07/25 Audio POP issue

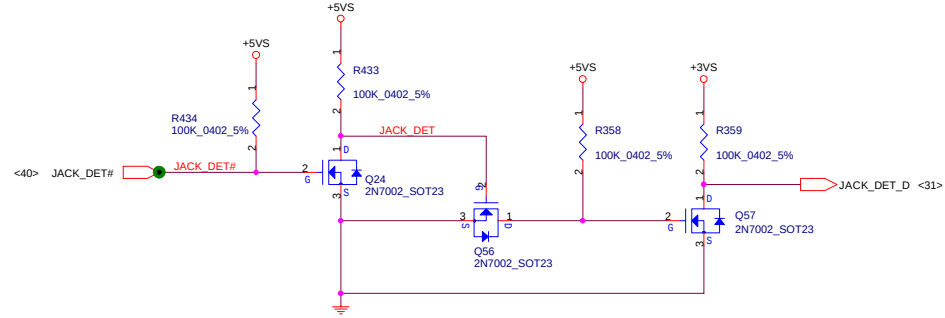
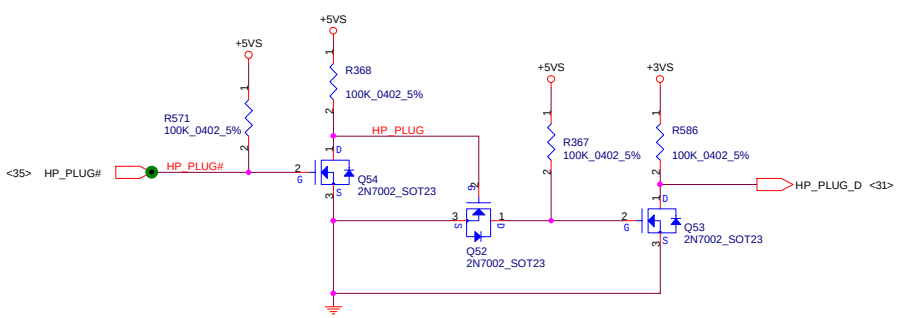
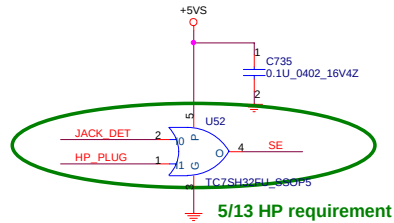
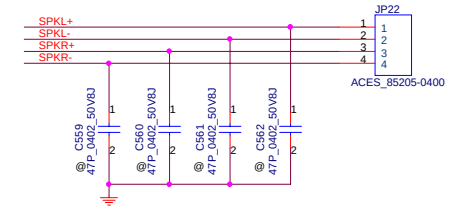
Security Classification				Compal Secret Data				Title			
Issued Date				2005/03/01				Deciphered Date			
								2005/04/06			
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Size				Document Number				Rev			
Custom				LA-2771				0.8			
Date				Tuesday, August 30, 2005				Sheet 31 of 53			



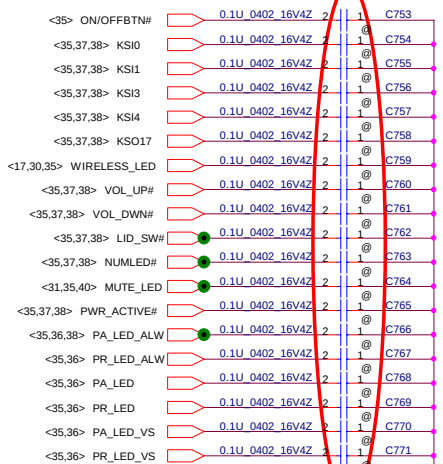
Security Classification	Compal Secret Data			Title	
Issued Date	2005/03/01	Deciphered Date	2005/04/06	AMOM_modem	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Custom	0.8
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Gain Settings			
GAIN0	GAIN1	SE/BTL#	Av(inv)
0	0	0	6 dB
0	1	0	* 10 dB
1	0	0	15.6 dB
1	1	0	21.6 dB
X	X	1	4.1 dB



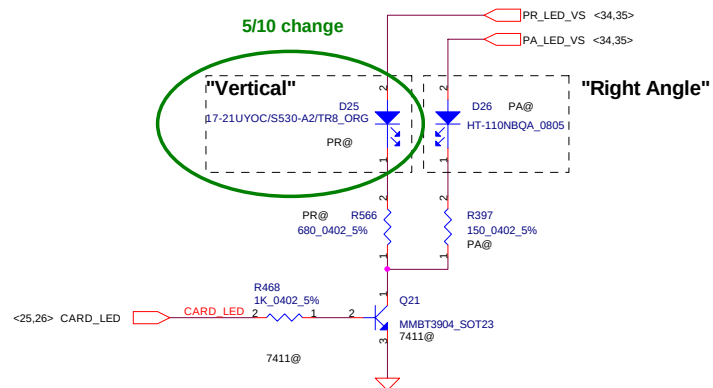
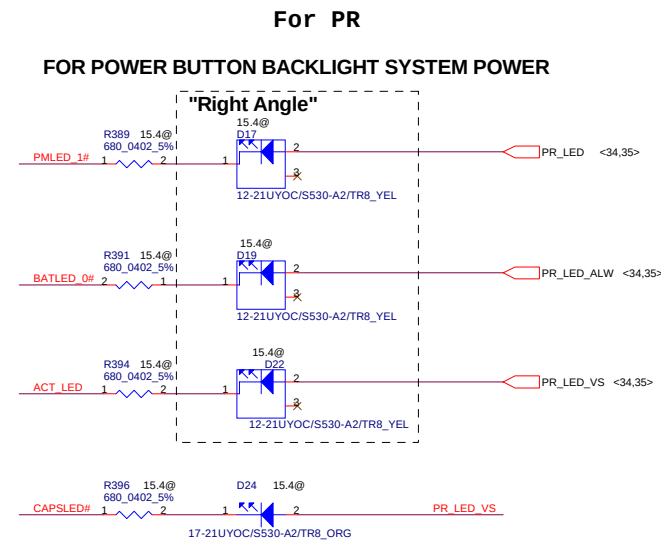
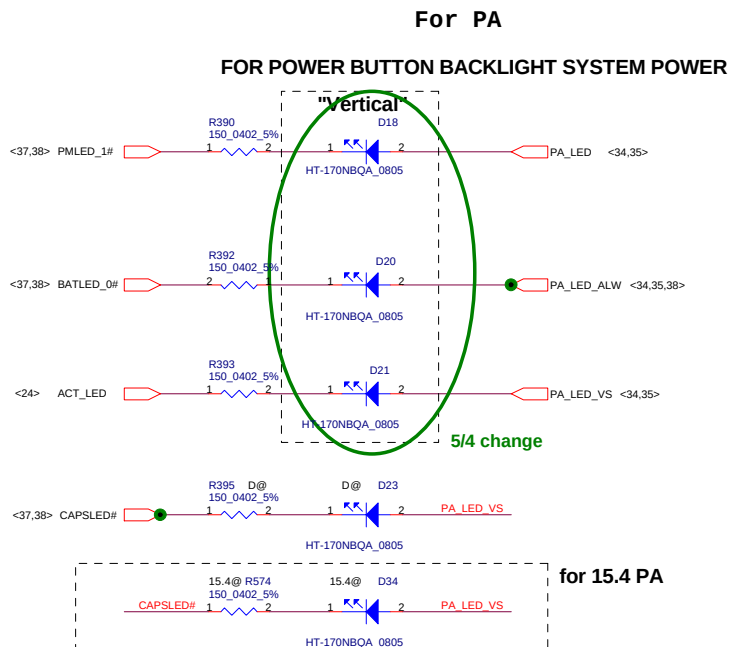
Security Classification		Compal Secret Data		Title	
Issued Date	2005/03/01	Deciphered Date	2005/04/06	AMP & Audio jack	
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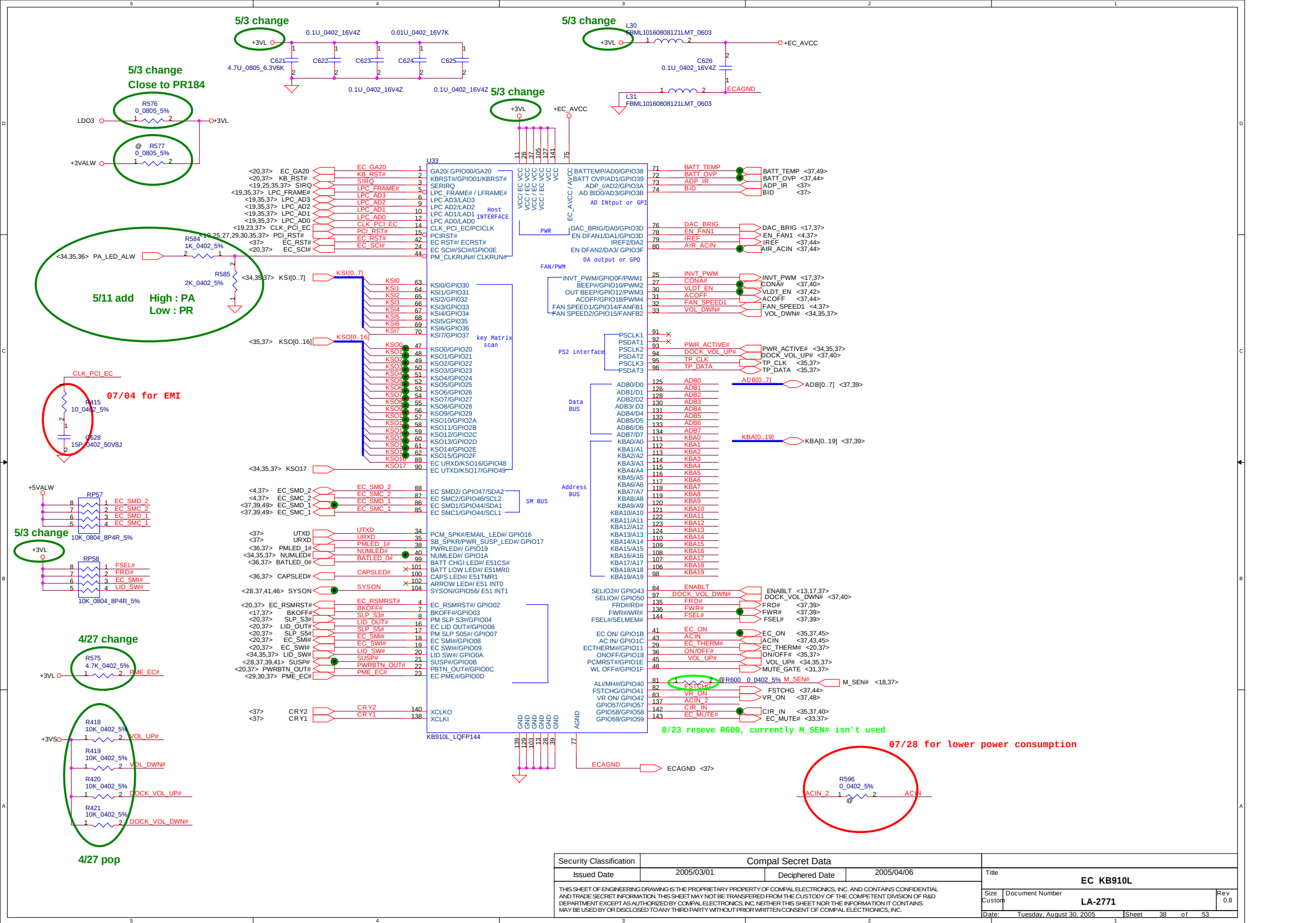
Security Classification	Compal Secret Data					
Issued Date	2005/03/01	Deciphered Date	2005/04/06	Title		
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				Size Custom	Document Number	Rev
					LA-2771	0.8
				Date:	Tuesday, August 30, 2005	Sheet 34 of 53



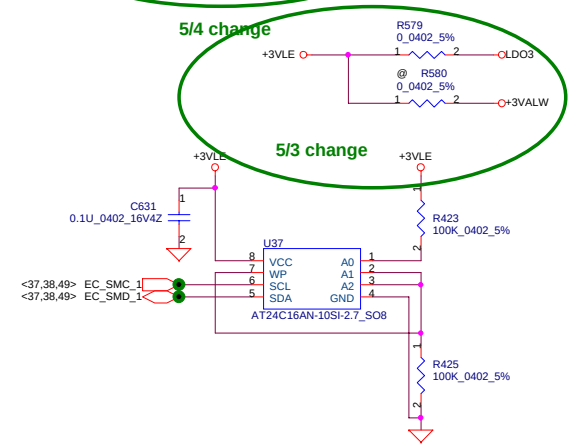
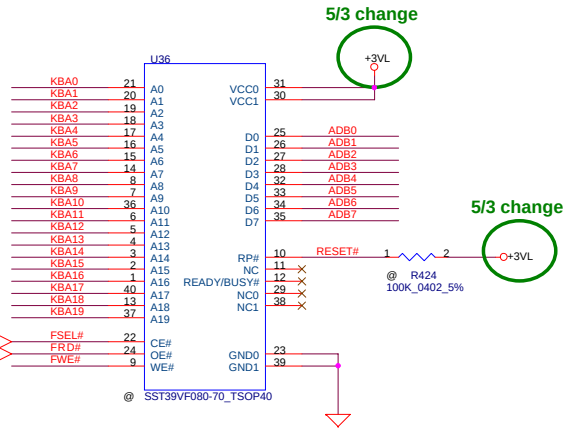
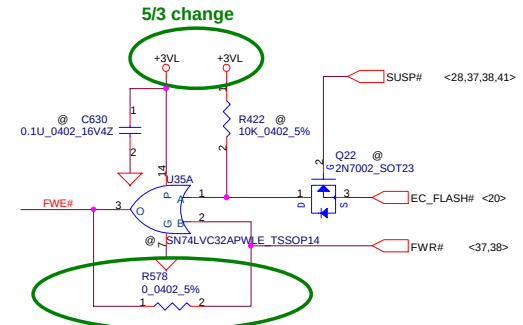
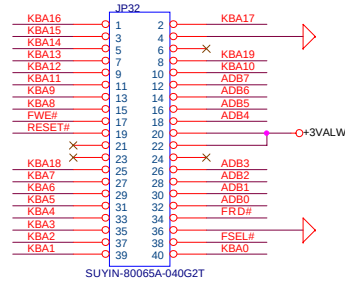
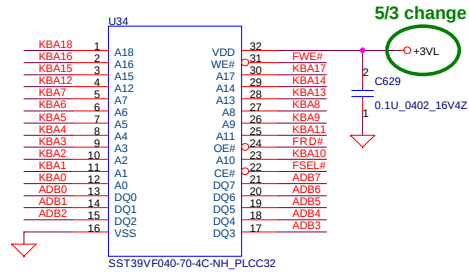
Security Classification	Compal Secret Data					
Issued Date	2005/03/01	Deciphered Date	2005/04/06	Title	KBD,ON/OFF,T/P,LED/B,DEBUG	
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				Custom	LA-2771	0.8
				Date:	Tuesday, August 30, 2005	Sheet 35 of 53



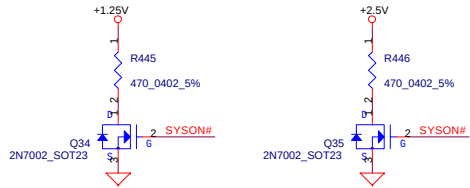
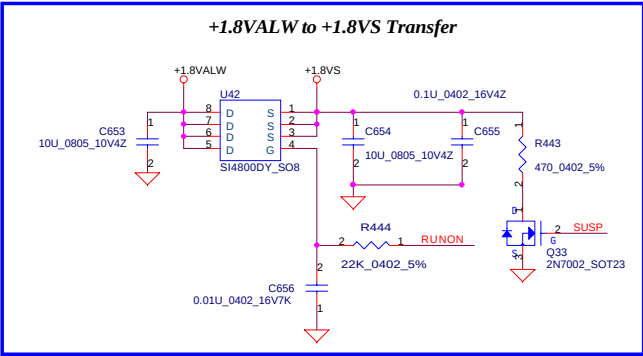
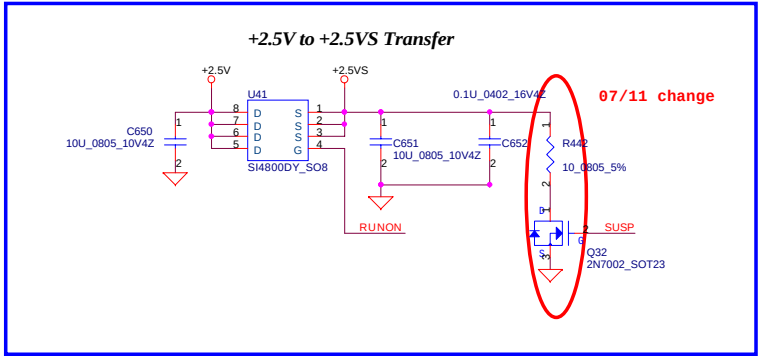
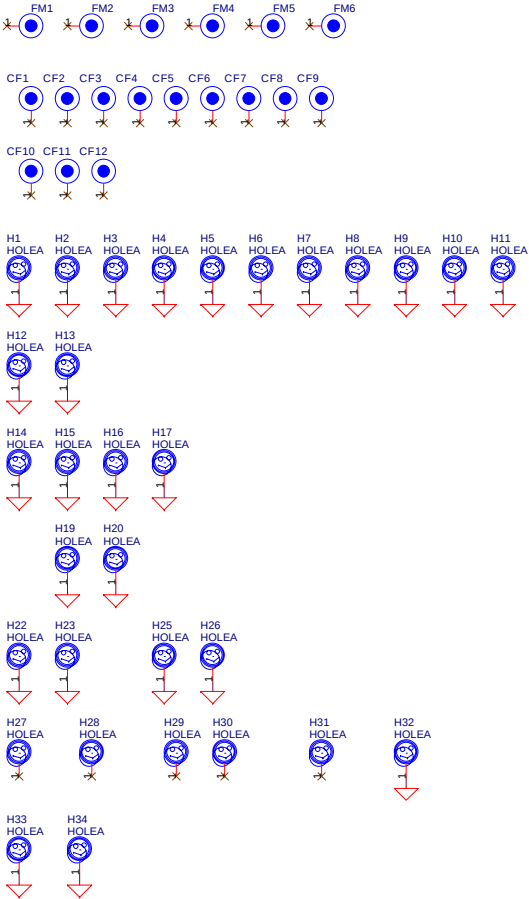
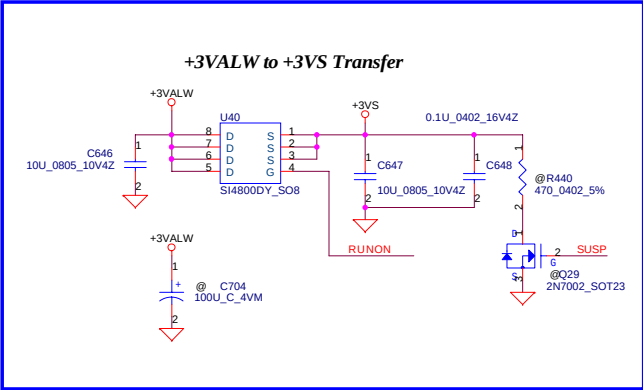
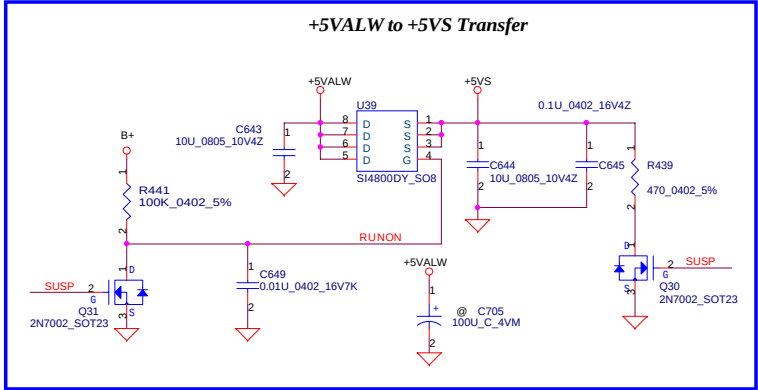
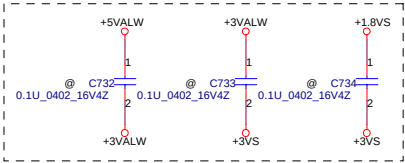
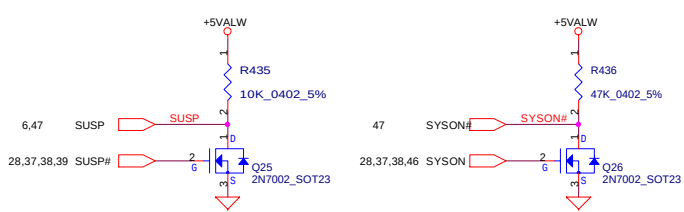
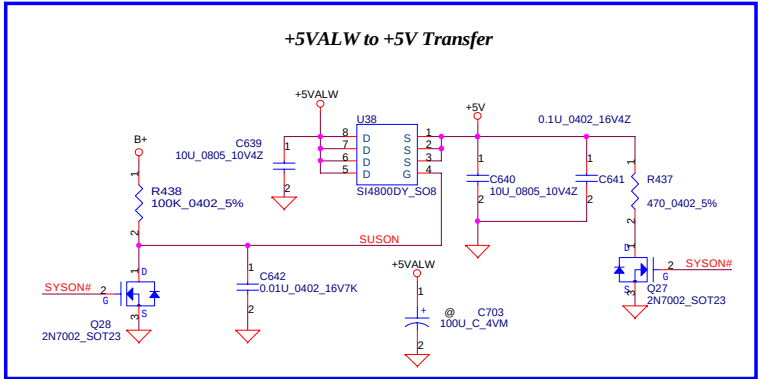
Security Classification	Compal Secret Data			Title	
Issued Date	2005/03/01	Deciphered Date	2005/04/06	INDICATE LED	
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				Custom	LA-2771
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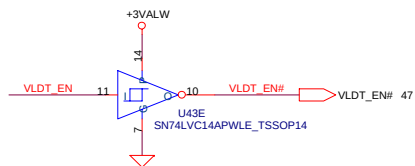
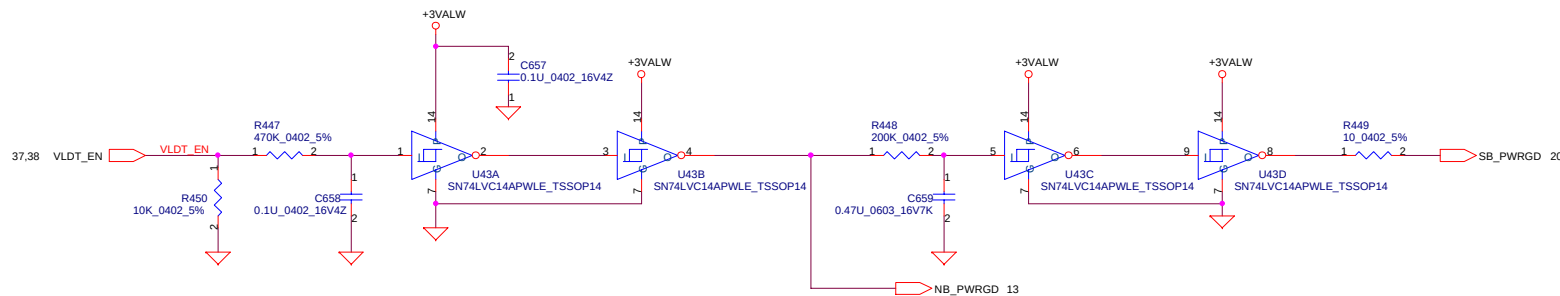
<37,38> ADB[0..7] ADB[0..7]
<37,38> KBA[0..19] KBA[0..19]



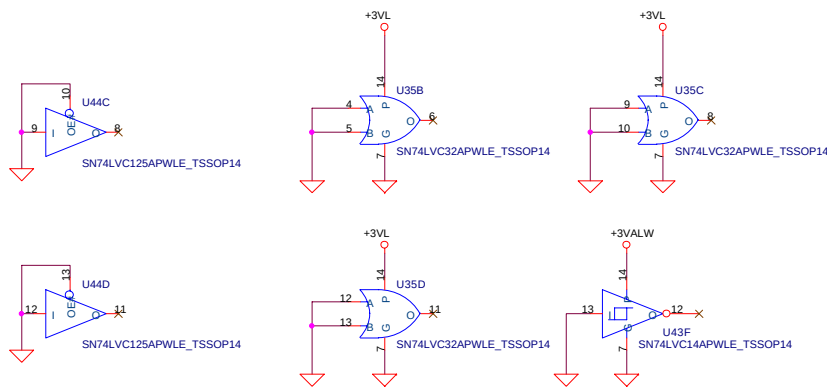
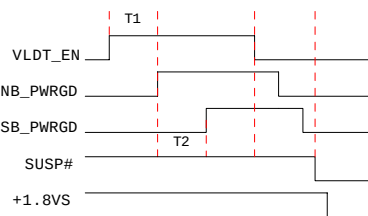
Security Classification	Compal Secret Data			Title	
Issued Date	2005/03/01	Deciphered Date	2005/04/06	BIOS & EC I/O Port	
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note:T1 minimum 15ms,T2 minimum 33ms/maximum 500ms,
SUSP# goes to low after SB_PWRGD goes to low for power
down.



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Detector

Vin Detector : Cayenne
 18.234 17.841 17.449
 17.597 17.210 16.813

Vin Detector : Boxster
 14.352 13.950 13.555
 13.818 13.411 13.000

8/29 Change to 0.022uF
 for AC Off issue

Cayenne : PR5=22K; PR6=19.6K
Boxster : PR5=47K; PR6=27K

ACIN : Cayenne

Precharge detector
 14.805 14.333 13.872
 13.355 12.933 12.465

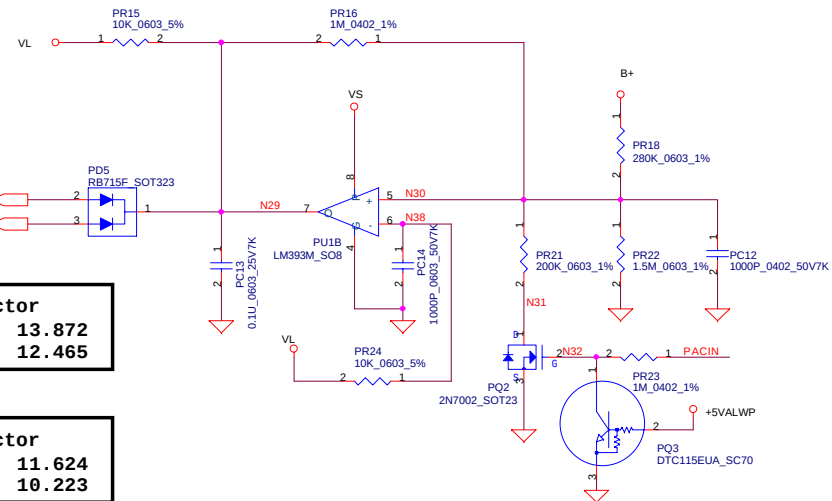
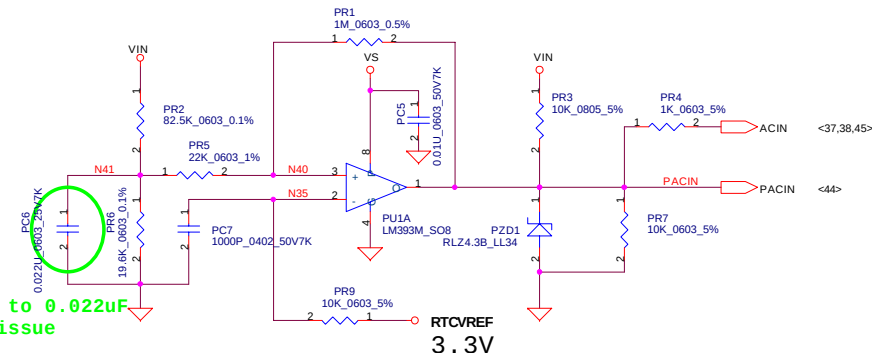
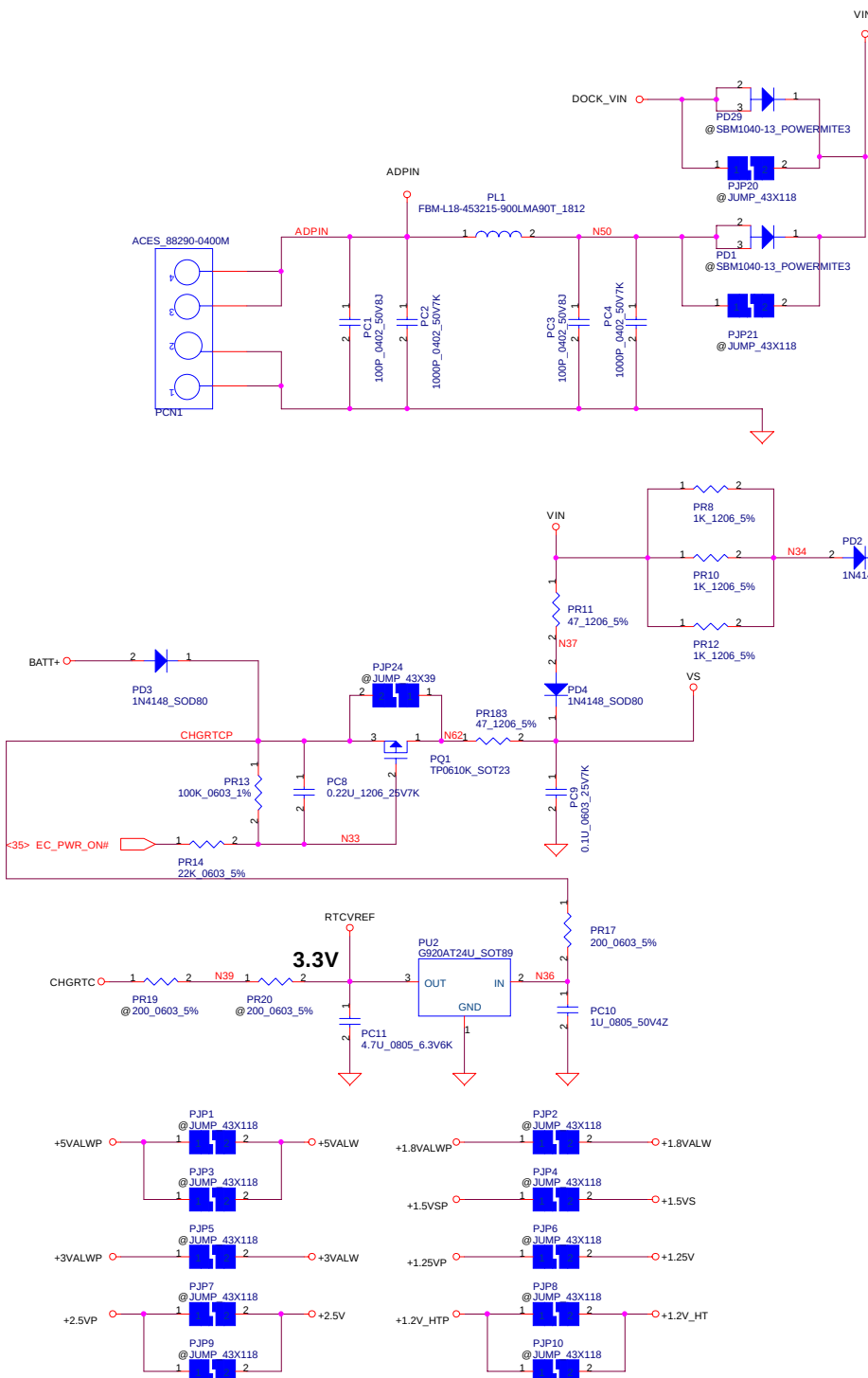
ACIN : Boxster

Precharge detector
 12.384 12.000 11.624
 10.927 10.600 10.223

BATT

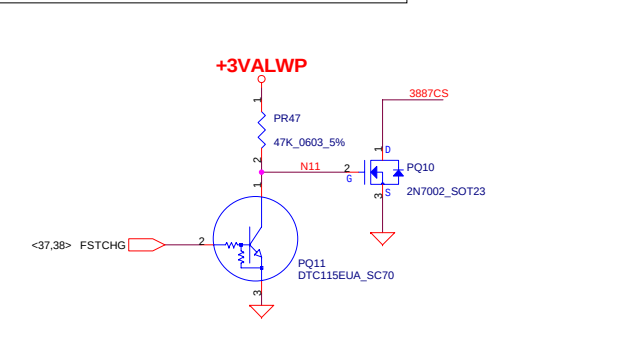
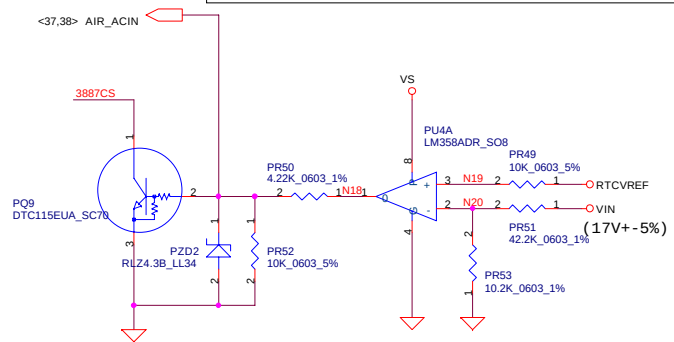
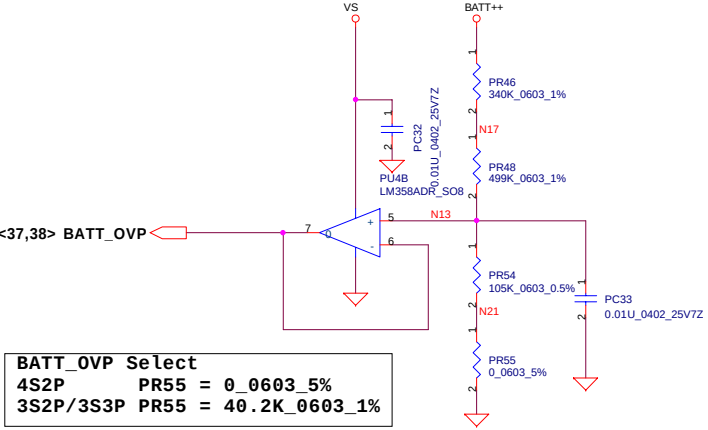
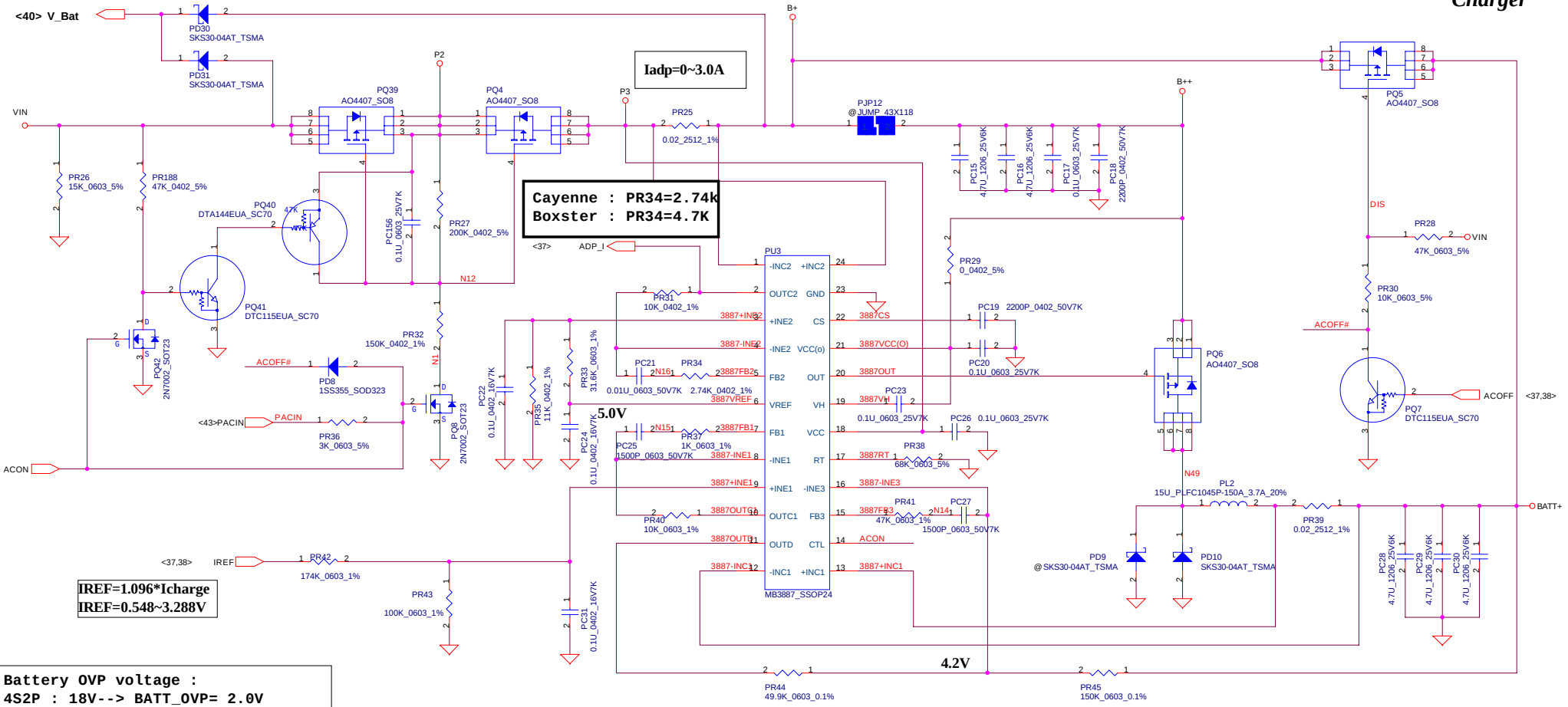
Detector
 7.558 7.333 7.112
 6.108 5.933 5.704

Cayenne : PR21=200K
Boxster : PR21=300K



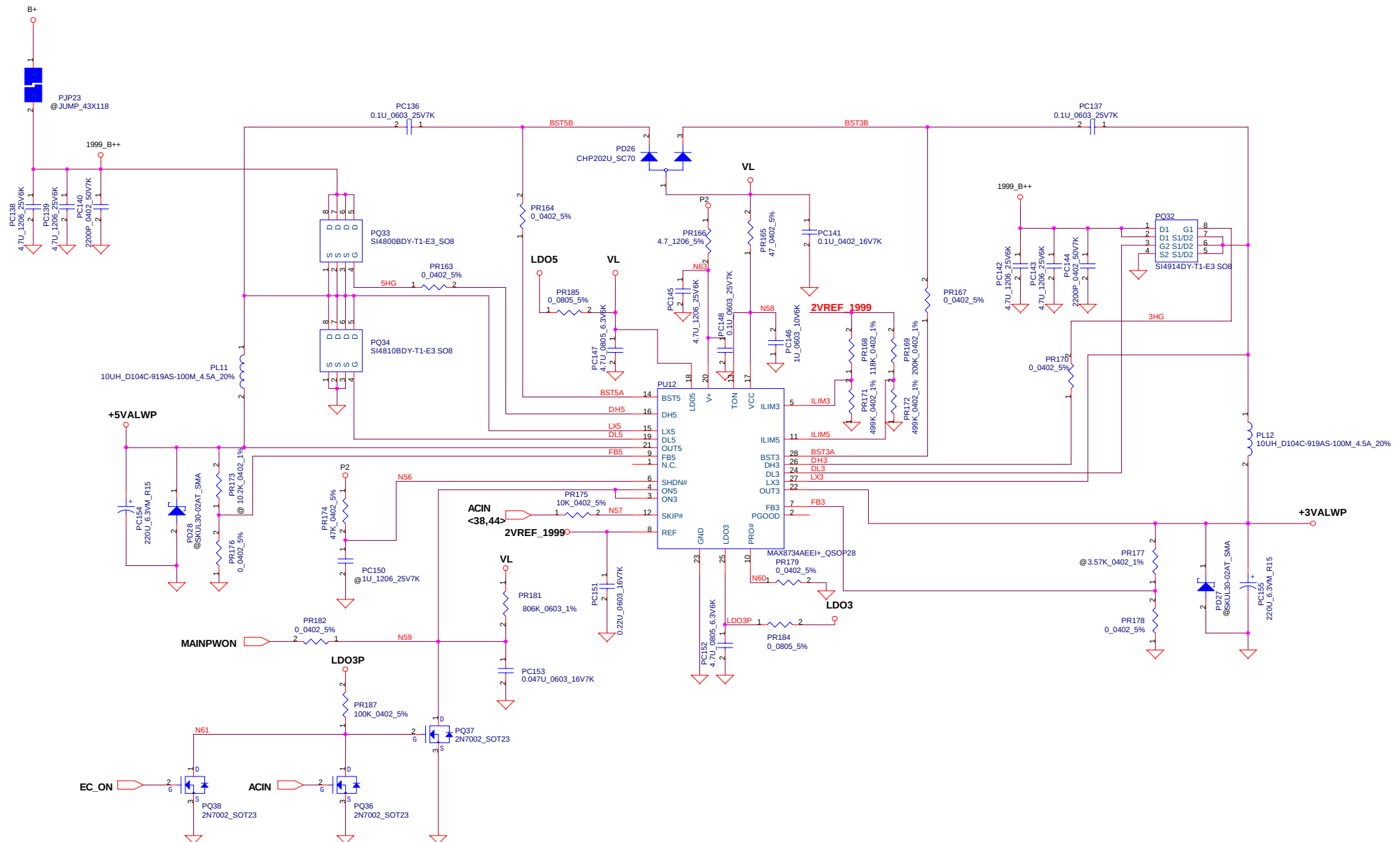
Security Classification		Compal Secret Data							
Issued Date		2005/03/01		Deciphered Date		2006/03/01		Title	
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								Size Custom	
Date:				Tuesday, August 30, 2005				Sheet 43 of 53	

Charger



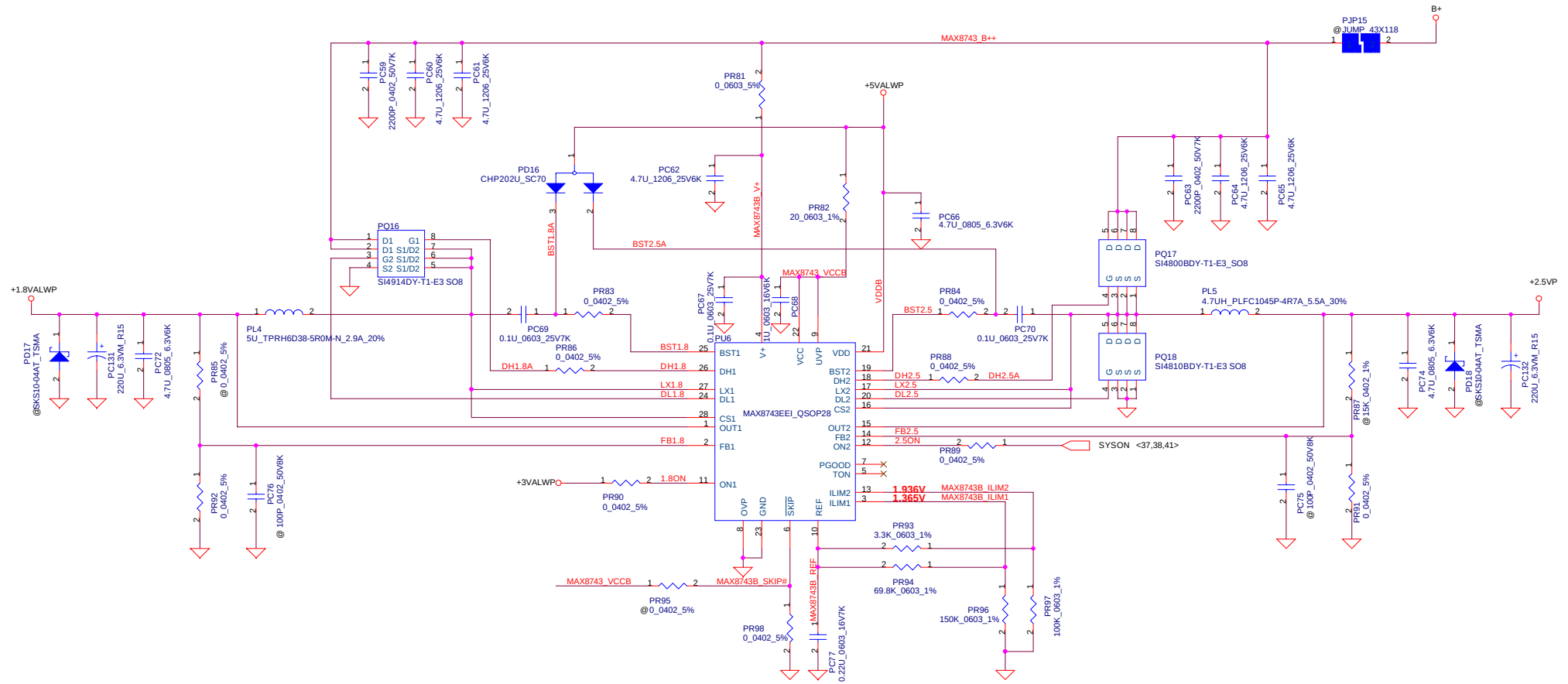
Security Classification				Compal Secret Data		Title	
Issued Date	2005/03/01	Deciphered Date	2006/03/01			Size	Document Number
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+3VALWP/+5VALWP



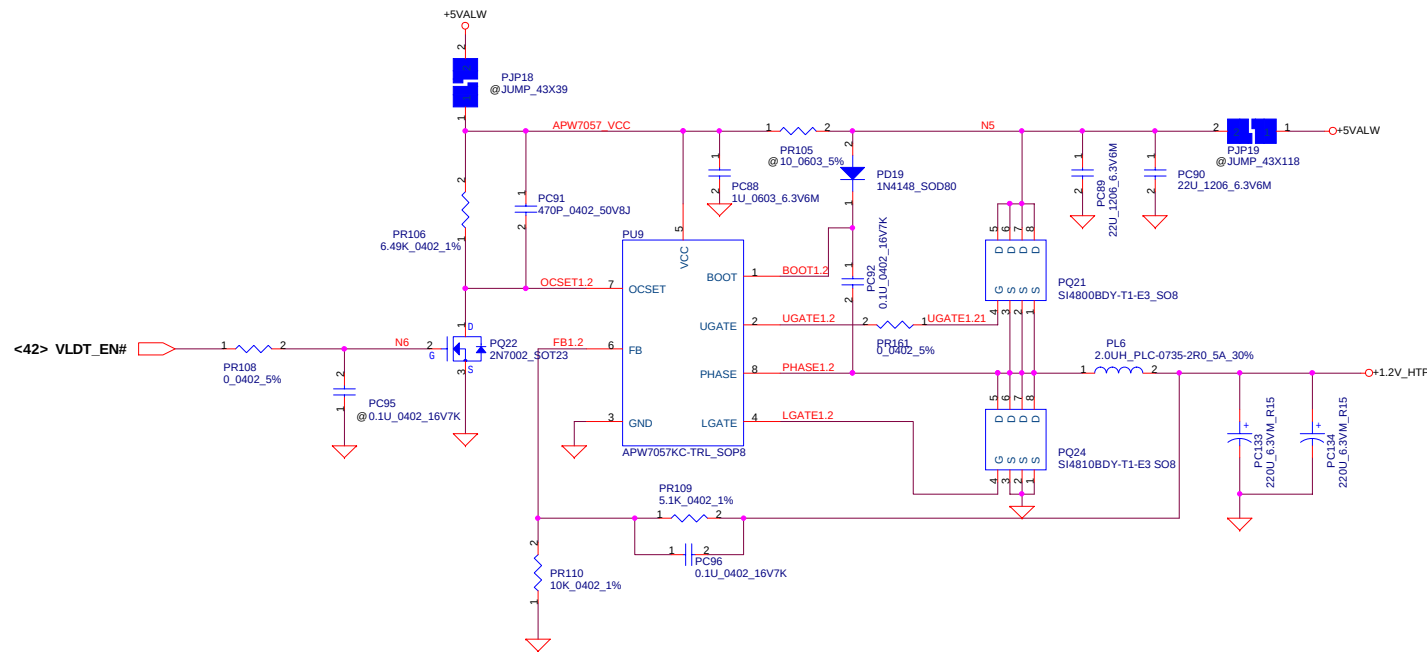
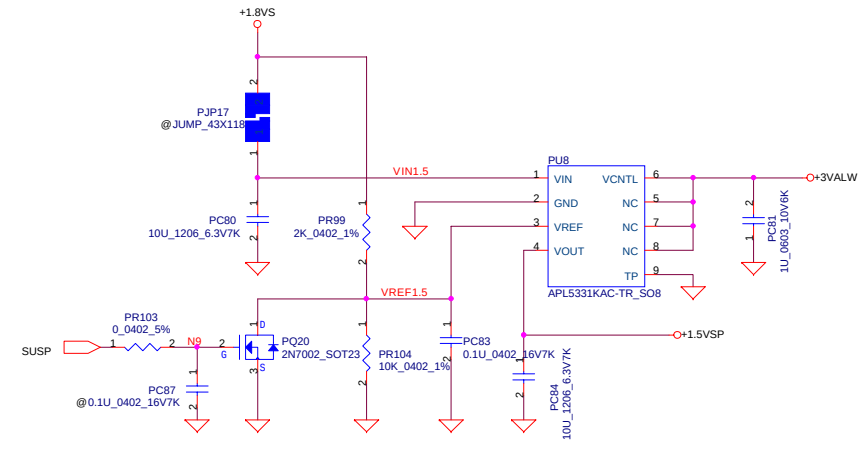
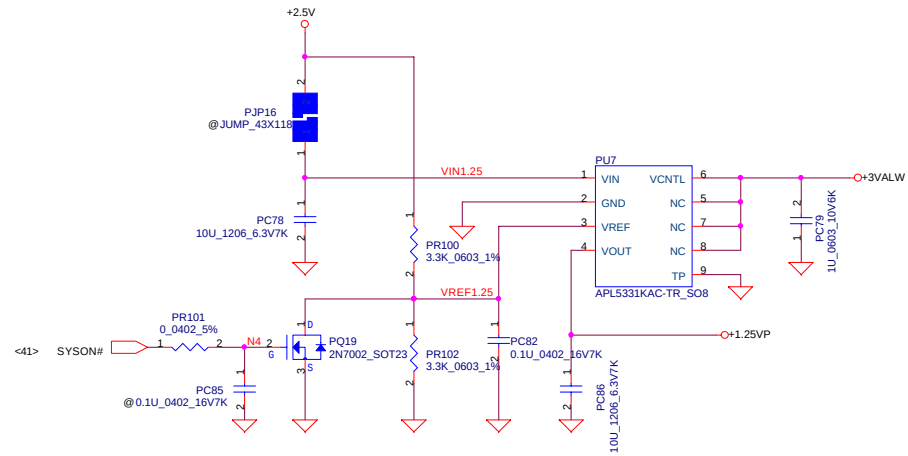
Security Classification		Compal Secret Data			
Issued Date	2005/03/01	Deciphered Date	2006/03/01	Title	+3VALWP/+5VALWP
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+1.8VALWP/+2.5VP

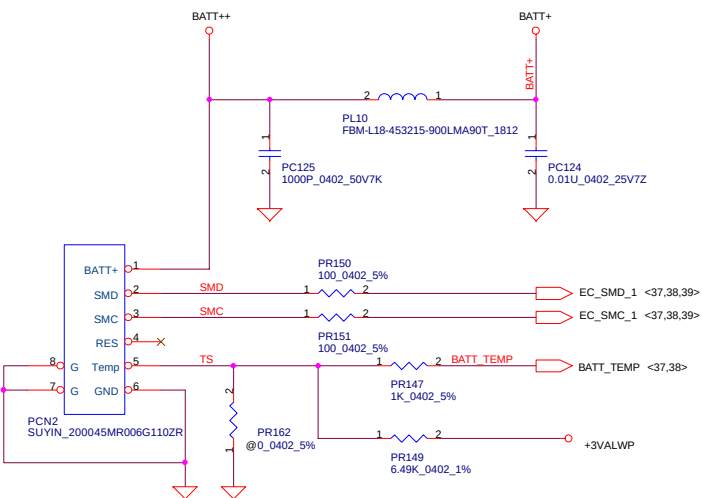


Security Classification		Compal Secret Data		Title	
Issued Date	2005/03/01	Deciphered Date	2006/03/01	+1.8VALWP/+2.5VP	
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+1.2V_HTP/+1.5VSP/+1.25VP



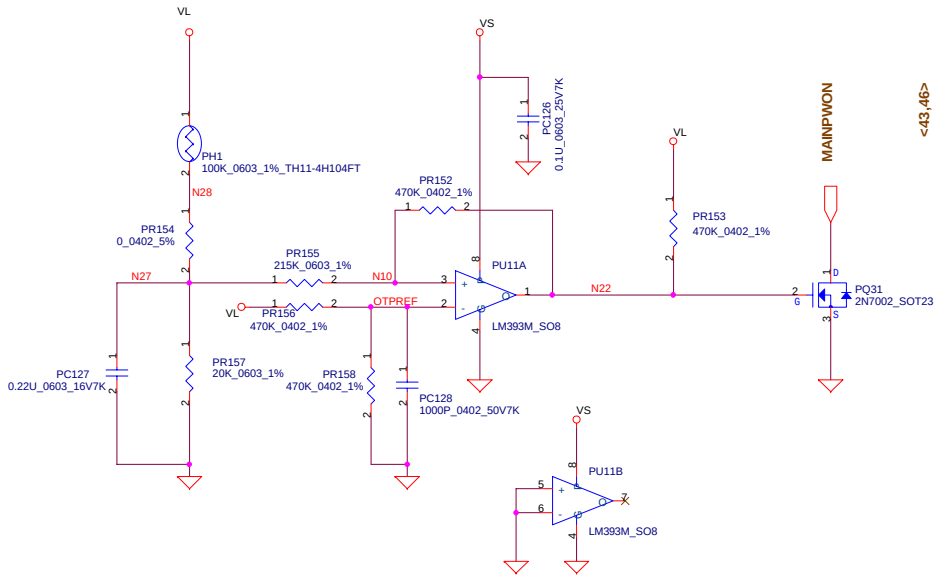
Security Classification		Compal Secret Data					
Issued Date	2005/03/01	Deciphered Date	2006/03/01	Title +1.2VSP/+1.5VP/+1.25VSP			
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PJPB1 battery connector
SMART
Battery:
1.BATT+
2.SMBD
3.SMBC
4.Res
5.Temp
6.GND

CPU

PH1 under CPU botten side :
CPU thermal protection at 90 +-3 degree C
Recovery at 50 +-3 degree C



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						Custor		LA-2771		0.8			
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Item	Reason for change	PG#	Modify List	Date	B.Ver#
1	Add the functionality to turn on the system on from Off and S4 with the consumer IR.	45	Change 3/5VALWP regulator from MAX1902 to MAX1999	2005.04.08	
2	Change PCN1 from SP020022200 to SP020024800.	49	Change PCN1 from SP02000A000 to DC040001P00.	2005.04.08	
3	Because EMI test fail	48	Change CPU_CORE HI-SIDE MOS from A04408 to FDS6294, LOW-SIDE from A04410 to FDS6676AS	2005.05.18	
4	Because V_bat to Calgary havn a leakage current, cause precharge can't finisn.	44	Add PD30 and PD31 to supply the V_bat power.	2005.06.09	
5	Add air-adapter detector	44	Add PU4 to detector air-adapter in.	2005.07.02	
6	Adjust the MB3887 CC to CP response cause the adapter OCP.	44	Change the PR31 from 100k_0402_1% to 10k_0402_1% Change the PR34 from 10k_0402_1% to 2.74k_0402_1% Change the PC21 from 4700p_0603_50V to 0.01u_0603_50v	2005.07.29	

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Compal Electronics, Inc.			
Title			
PWR PIR			
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Item	Fixed Issue	Reason for change	PAGE	Modify List	M.B. Ver.
<2005.04.18>	1	HP Jack + SPDIF change type	31 , 33	Del : U27 , C563 , C564 Add: Q52 , Q53 , Q54 , Q56 , Q57 , R571 , C730 , C731 , R567 , R569 , R568 , R570	0.2
	2	Support CIR wake up from battery mode	4 , 37 , 38 , 41 , 35	12VALW change to B+ +3VALW change to LDO3 , +5VALW change to LDO5	0.2
	3	for Marvell express card Rest timing	20 , 28	Add: Q55	0.2
	4	for some HDD's LED alway on	24	Del: D29 , R259 , Q10	0.2
	5	for card bus can't work	27	S1_VCC and S1_VPP change to +S1_VCC and +S1_VPP	0.2
	6	For nissan common design	35	JP28 and JP29 pin swap	0.2
	7	For keyboard issue	37	Del: D27	0.2
<2005.04.27>	1	wake up from LAN	38	Add: R575(Pull UP PME_EC#)	0.3
	2	For VOL_UP,DOWN function issue	38	POP: R418,R419,R420,R421	0.3
<2005.05.3>	1	For CIR wake up	38	Add: R576, R577 to option	0.3
<2005.05.4>	1	EC 910L include portion circuit	39	Add: R578 , Del:C630 , R422 , Q22 , U35A	0.3
	2	ME change LED type	36	D18,D20,D21 from right angle change to vertical type	0.3
<2005.05.6>	1	Add wireless LED current limit resister	17	Add: R581 current limit resister	0.3
<2005.05.9>	1	TP conn pin reverse	35	Pin swap	
<2005.05.10>	1	ME change LED type	36	D25 from right angle change to vertical type	
	2	HP requirement from 75 ohm change to 33 ohm	33	R567 , R569 from 75 ohm change to 33 ohm	
	3	For nissan common design	35	Add: R582 , R583	
<2005.05.11>	1	For 2 way and 4 way touch pad	36	Add: R584 , R585	
<2005.06.10>		update JP23 dual USB connector to reverse type			0.4
		DFX modification for Rev0.4 MB			
		Change MR954, MC928, MR924, MR922, MR932, MC944, MC970, MC976, MC978, MC926 to correct pad size with Compal layout rule			
		update USB connector to reverse type			
		Change D6 form RB411 to RB491			

Security Classification		Compal Secret Data		Title	
Issued Date	2005/03/01	Deciphered Date	2005/04/06	HW PIR	
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5					4					3					2					1										
Item					Fixed Issue					Reason for change					PAGE					Modify List					M.B. Ver.					
<2005.06.27>					1						For clear H_RST# glitch					41					Add Q61					0.5				
<2005.07.04>					1						For +1.8VS EMI					11					Change R61, R84 value from 0 ohm to 120 ohm					0.5				
					2						For +1.2V_HT power ripple					14					Change C197, C209, C254, C248, C251, C256, C258 value from 0.1U 16V4Z 0402 to 1U 6.3V4Z 0402					0.5				
					3						For +2.5VS EMI					15					Add C737, C738, C741, C742 220P 50V8J 0402					0.5				
					4						For +2.5VS EMI					15					Add C739, C740, C743, C744 1000P 50V7K 0402					0.5				
					5						For Mini-PCI_CLK EMI					30					Mount R325 and C512									
					6						For CardBus chip PCI_CLK EMI					25					mount R279 and C442					0.5				
					7						For LAN chip PCI_CLK EMI					29					Mount R322 and C504					0.5				
					8						For EC K/B chip PCI_CLK EMI					30					Mount R415 and C628									
<2005.07.05>					1						For USB EMI					34					Add L33, L34									
					2						For USB EMI					34					change R561,R559,R562,R560 size from 0603 to 0402									
<2005.07.07>					1						For dock Docking audio noise					31					Add Q58, R589, Q60, R590, Q59, R589									
					2						For Dock_LOUTR/L EMI					40					Add R591, C745, R592, C746									
					3						For Docking TV_out signals EMI					40					Add R593, C747, R594, C748,C749, C750, R595, C751, C752									
<2005.07.11>					1						For accelerate +2.5VS discharge speed					40					change R442 size and value from 470_0402 to 10_0805									
					2						For LAN lamp					29					Swap Activity and Link Lamp									
<2005.07.25>					1						For SanDisk SD 256M card could not work issue					25					delete U48 Quick switch reserve schematics									
					2						For SanDisk SD 256M card could not work issue					25					Delete R463, R465, R466, R476, R268									
					3						For SanDisk SD 256M card could not work issue					26					Delete R270, R267, R547, R548									
					4						For SanDisk SD 256M card could not work issue					26					Mount R550 and R549									
<2005.07.28>					1						For lower power consumption					38					Add and un-mount R596									
					2						For CRT Assy					18					change CRT footprint									
																										</				

[illegible]